

# Fusion Trees

## Part Two

Recap from Last Time

# Our Machine Model

- We will assume we're working on a machine where memory is segmented into  $w$ -bit words.
  - Although on any one fixed machine  $w$  is a constant, in general, don't assume this is the case. 32-bit was the norm until fairly recently, and before that 16-bit was standard.
- We'll assume C integer operators work in constant time, and won't assume other integer operations (say, finding most significant bits, counting 1 bits set) are available.

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# Word-Level Parallelism

- Last time, we saw five powerful primitives built using word-level parallelism:
  - **Parallel compare:** We can compare a bunch of small numbers in parallel in  $O(1)$  machine word operations.
  - **Parallel tile:** We can take a small number and “tile” it multiple times in  $O(1)$  machine word operations.
  - **Parallel add:** If we have a bunch of “flag” bits spread out evenly, we can add them all up in  $O(1)$  machine word operations.
  - **Parallel rank:** We can find the rank of a small number in an array of small numbers in  $O(1)$  machine word operations.
  - **Most-significant bit:** We can compute  $\text{msb}(n)$  for any  $w$ -bit integer  $n$  in  $O(1)$  machine word operations.

# Integer LCP

- Computing `msb` efficiently lets us implement a number of other efficient primitives.
- Given two integers  $m$  and  $n$ , the **longest common prefix** of  $m$  and  $n$ , denoted  $\text{lcp}(m, n)$ , is the length of the longest bitstring they both start with.
- **Claim:** We can compute this in time  $O(1)$ .

00011010 01101110 01111000 01001101 00101111 00001101 01110111 01100001

⊕ 00011010 01000101 00010100 00100000 01010000 00100010 01000100 00001000

00000000 00101011 01100100 01101101 01111111 00101111 00110011 01101001

$$w - 1 - \text{msb}(m \oplus n)$$

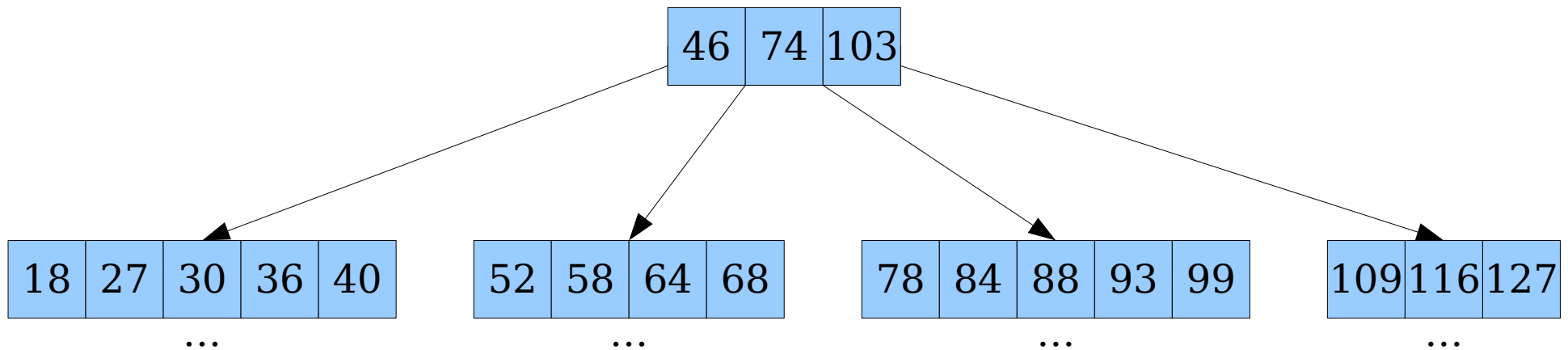
New Stuff!

# The Sardine Tree Revisited

- Last time, we designed a data structure nicknamed the *sardine tree* that
  - stores  $s$ -bit keys, where  $s$  is much smaller than  $w$ , and
  - supports all operations in time  $O(\log_{w/s} n)$ .
- Our goal for today will be to generalize this to work with arbitrary integer keys, not just  $s$ -bit keys.

# The Sardine Tree Revisited

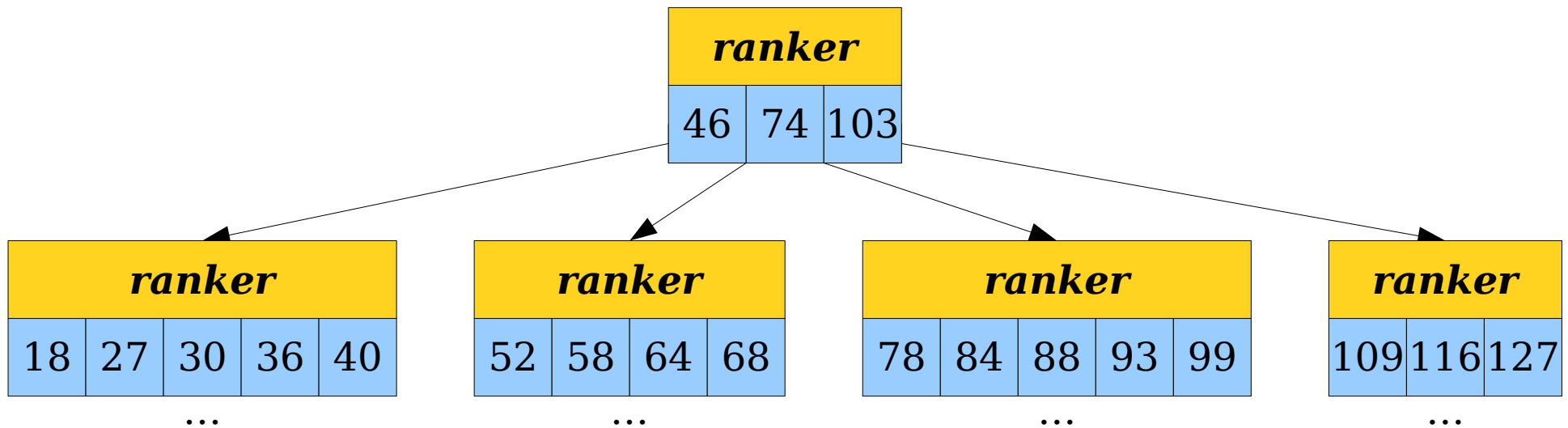
- At a high level, the sardine tree is a B-tree augmented with extra information to support fast rank queries.
- The branching factor is  $\Theta(w / s)$ , the number of keys we can fit into a single machine word.
- We use a *parallel rank* operation at each node to determine which keys to check and which child to descend into.
- Therefore, each operation's cost is  $O(\log_{w/s} n)$ :  $O(1)$  work per each of  $O(\log_{w/s} n)$  nodes visited.





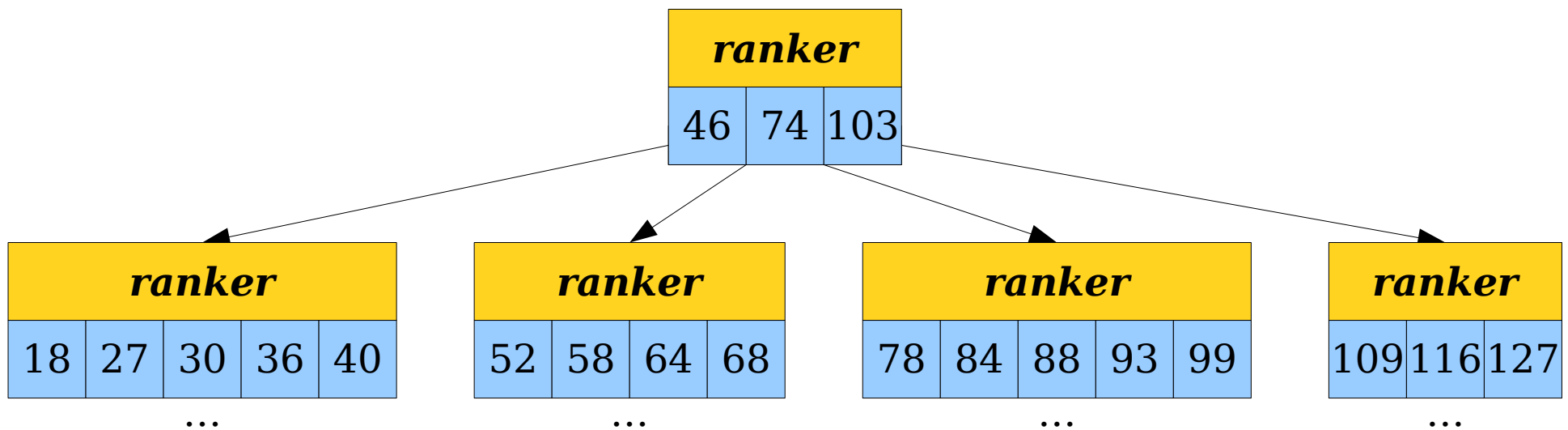
# The Sardine Tree Revisited

- The sardine tree is a specific case of a more general framework.
- Build a B-tree where each node is augmented with a data structure called a **ranker** with the following properties:
  - The ranker stores  $\Theta(K)$  total keys.
  - It supports queries of the form **rank**( $x$ ), which returns the rank of  $x$  among those keys, in time  $O(1)$ .
- The cost of performing a search is then  $O(\log_K n)$ , since the tree height is  $O(\log_K n)$  and we do  $O(1)$  work per node.



# The Sardine Tree Revisited

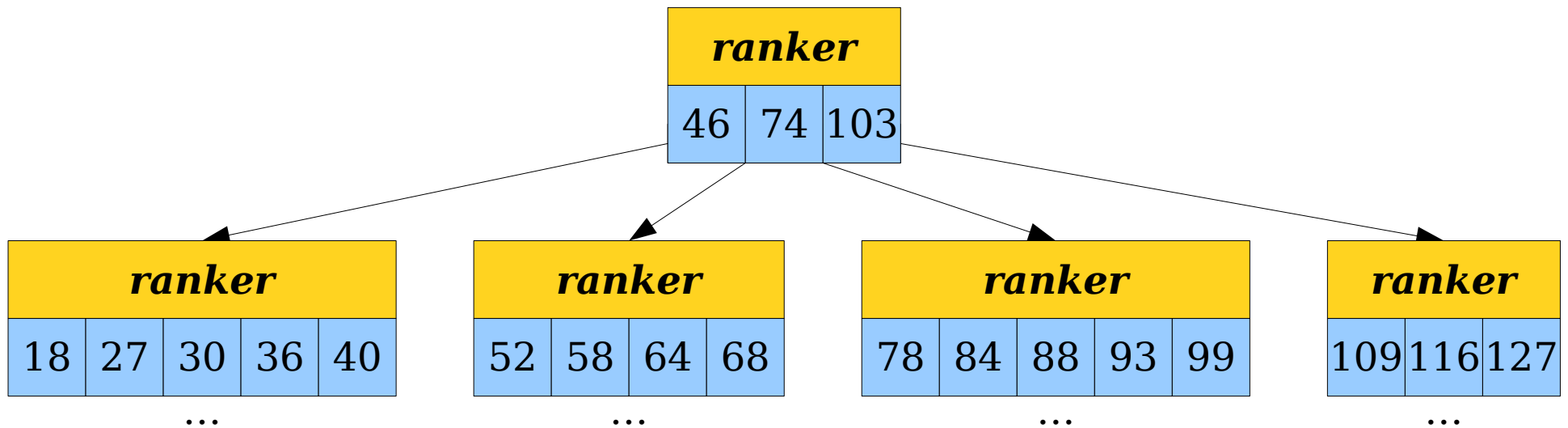
- The sardine tree ranker works by packing the  $\Theta(w/s)$  keys into a machine word, then using our *parallel rank* operation from last time.
- Since there are  $\Theta(w/s)$  keys per node, the runtime of each B-tree operation is  $O(\log_{w/s} n)$ , though the keys are severely size-limited.



# Fusion Trees

- The ***fusion tree*** is a B-tree augmented with a ranker that stores  $w^\epsilon$  keys for some constant  $\epsilon$  we'll pick later. Its keys are full  $w$ -bit words.
- The cost of a lookup, successor, or predecessor in a fusion tree is therefore

$$O(\log_w^\epsilon n) = O(\log n / \log w^\epsilon) = \mathbf{O(\log_w n)}.$$



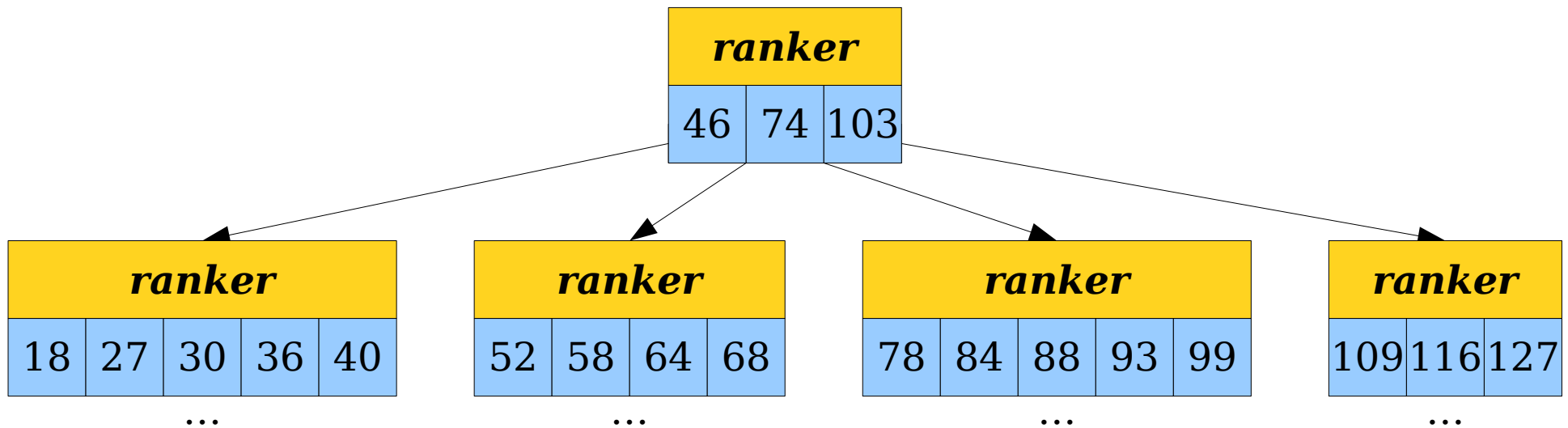
# Where We're Going

- The sardine tree solves the following problem:

**Support rank queries for a *large* number of *small* keys.**

- To build the fusion tree, we'll solve this problem:

**Support rank queries for a *small* number of *large* keys.**



# Where We're Going

- The *parallel rank* operation we devised last time permits  $O(1)$ -time rank queries, provided that all the keys fit into a machine word.
- In general, we can't assume that a collection of arbitrary keys all fit into a machine word.
- **Goal:** Compress multiple  $w$ -bit keys so that
  - they fit in a machine word so we can use *parallel rank*, and
  - the compression preserves enough information about their order so that the ranks we get back are meaningful.

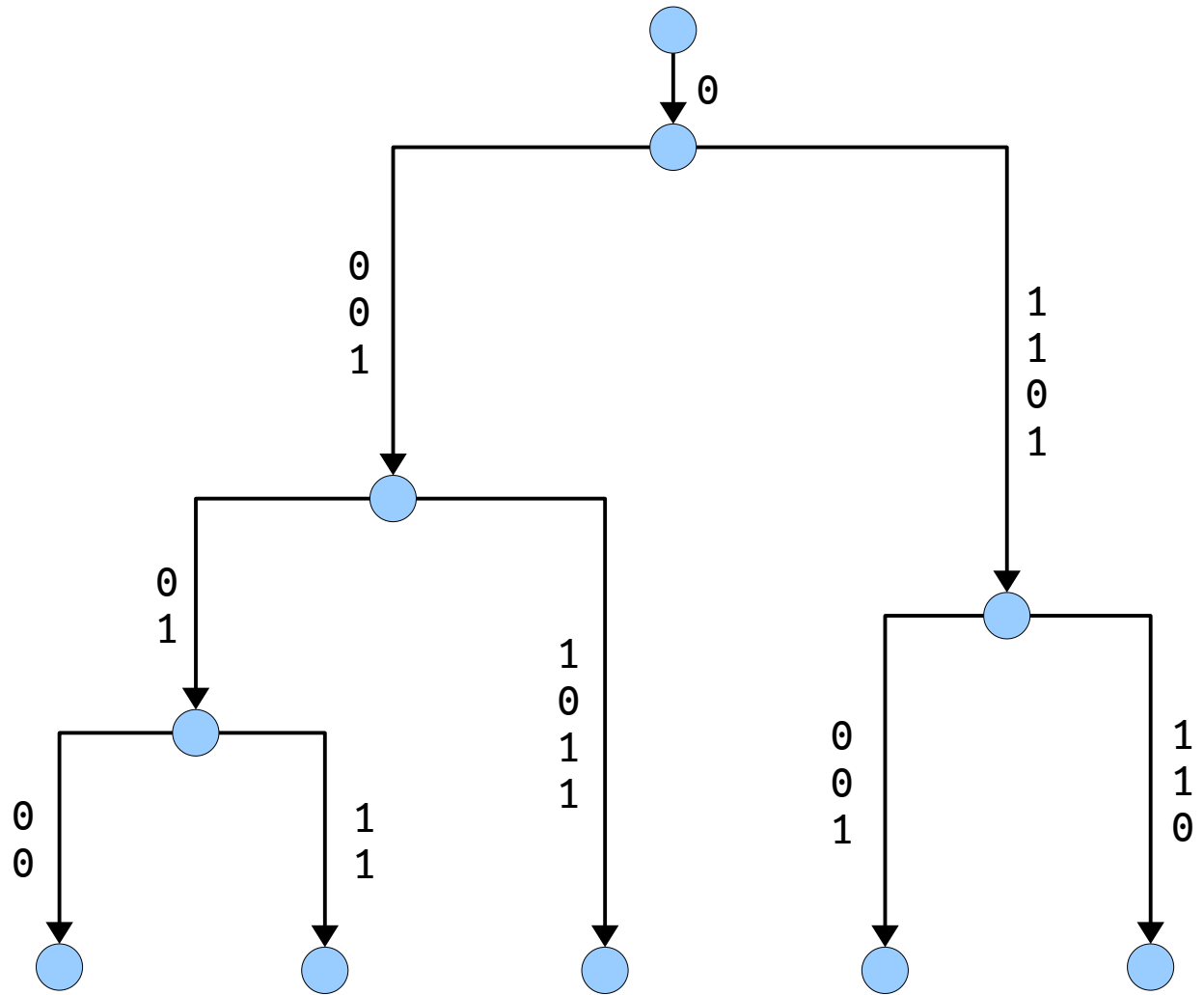
# Compressing Our Numbers

- Let's imagine we have a collection of  $w^\epsilon$  numbers, each of which is  $w$  bits long.
- For simplicity, we're going to assume that those numbers are given to us in advance and in sorted order.
  - We'll relax this later on.

00010100	00010111	00011011	01101001	01101110
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# Back to Tries

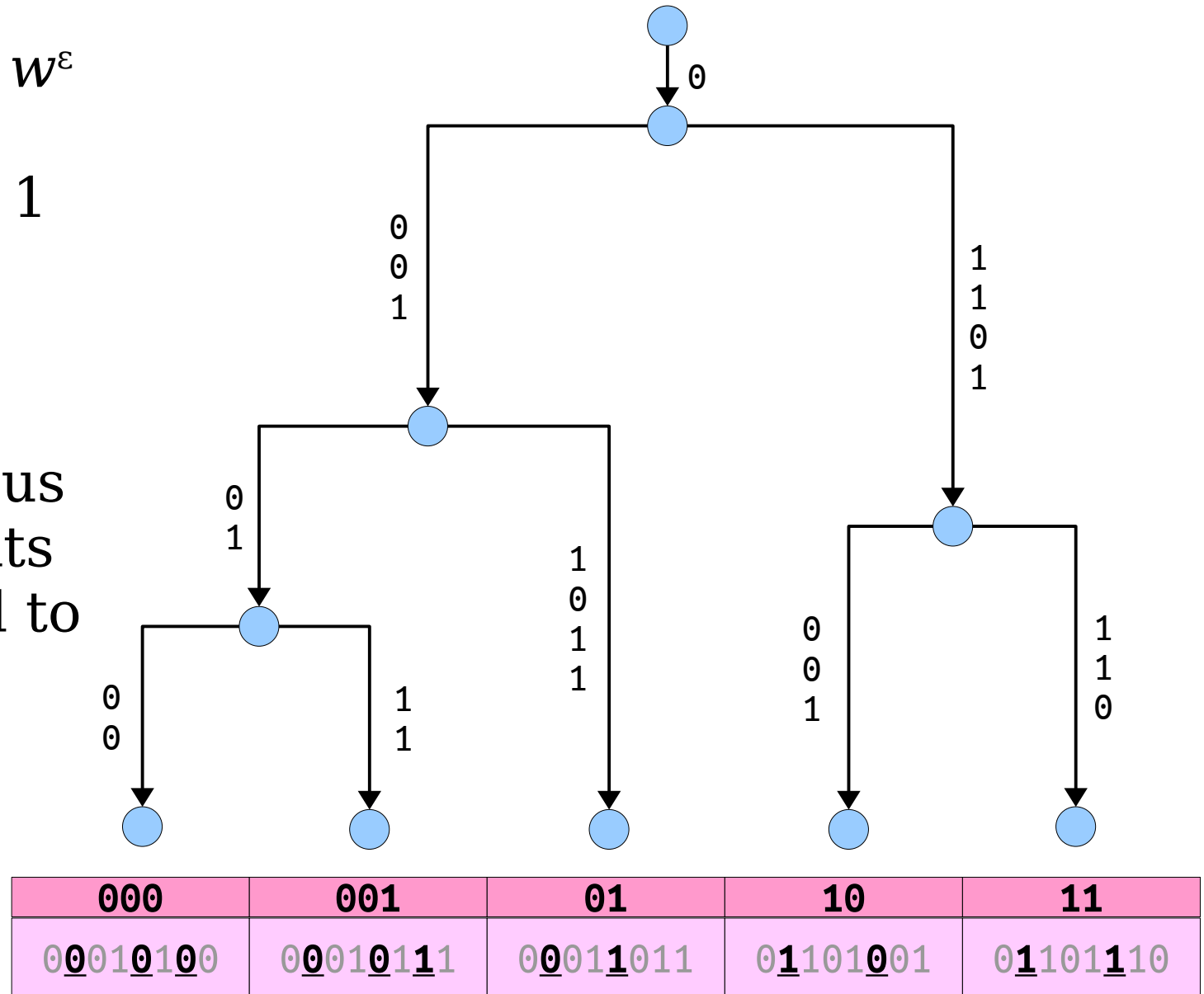
- Think about what happens if we make a trie from these numbers.
- We have few numbers ( $w^\epsilon$ ) and these numbers are large (size  $w$ ), so most nodes will have one child.
- **Idea:** Use a Patricia trie!



00010100	00010111	00011011	01101001	01101110
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# Back to Tries

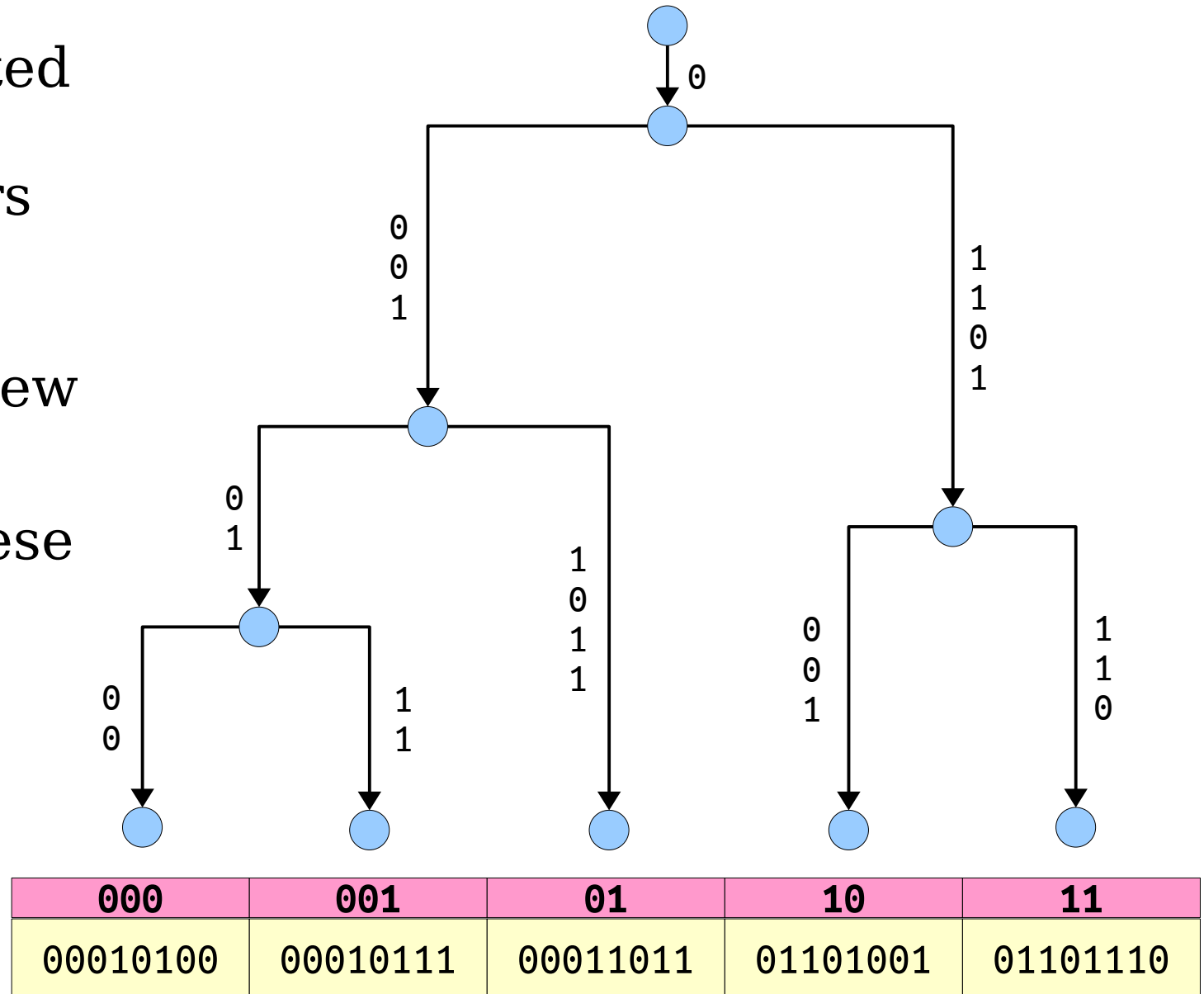
- Since there are  $w^\epsilon$  numbers, there are exactly  $w^\epsilon - 1$  junctions in the Patricia trie.
- Look at each number and focus purely on the bits that correspond to those junctions.





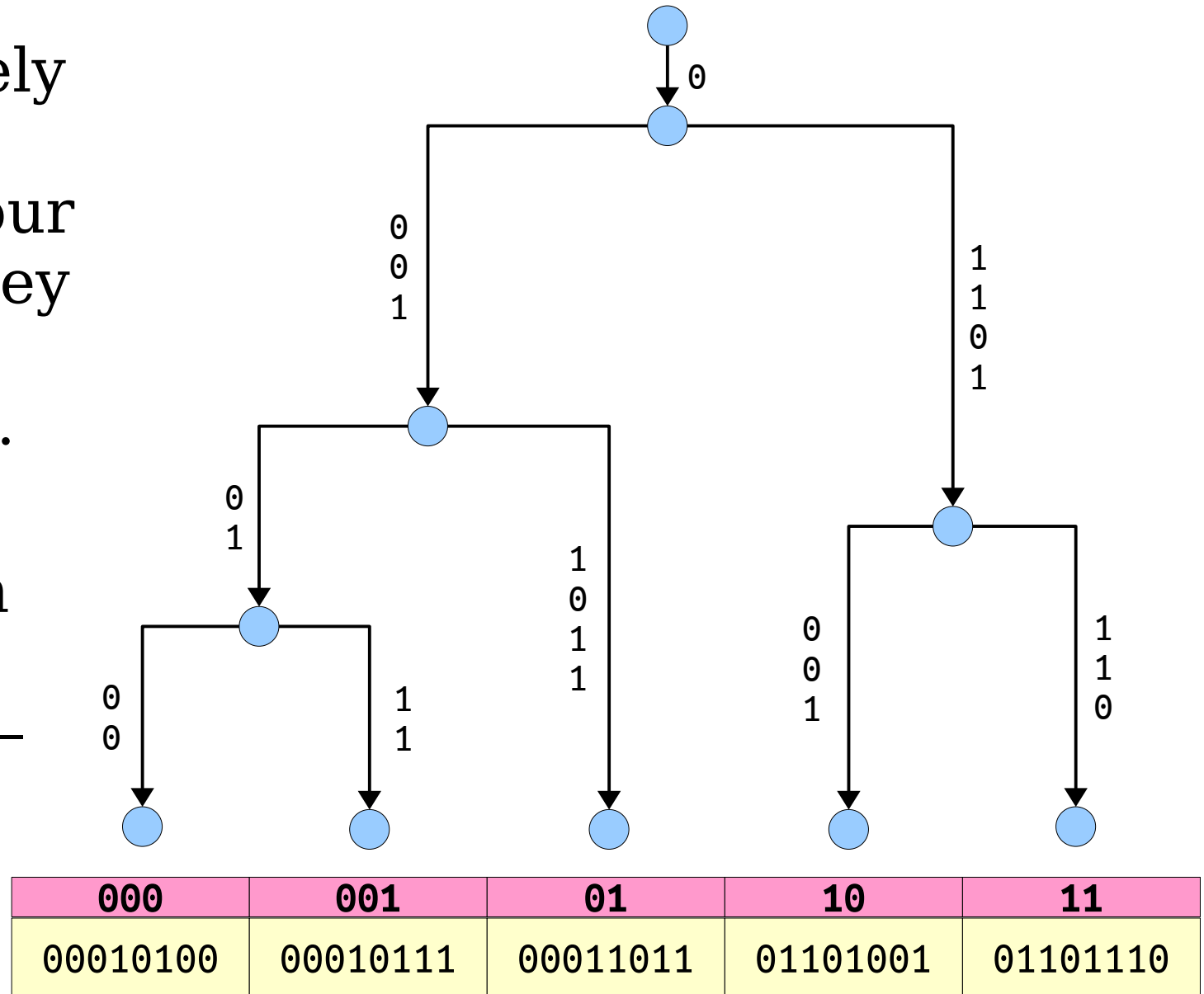
# Back to Tries

- **Claim:** The sorted order of these original numbers matches the *lexicographical* order of these new bitstrings.
- **Proof idea:** These new bitstrings represent paths through the Patricia trie.



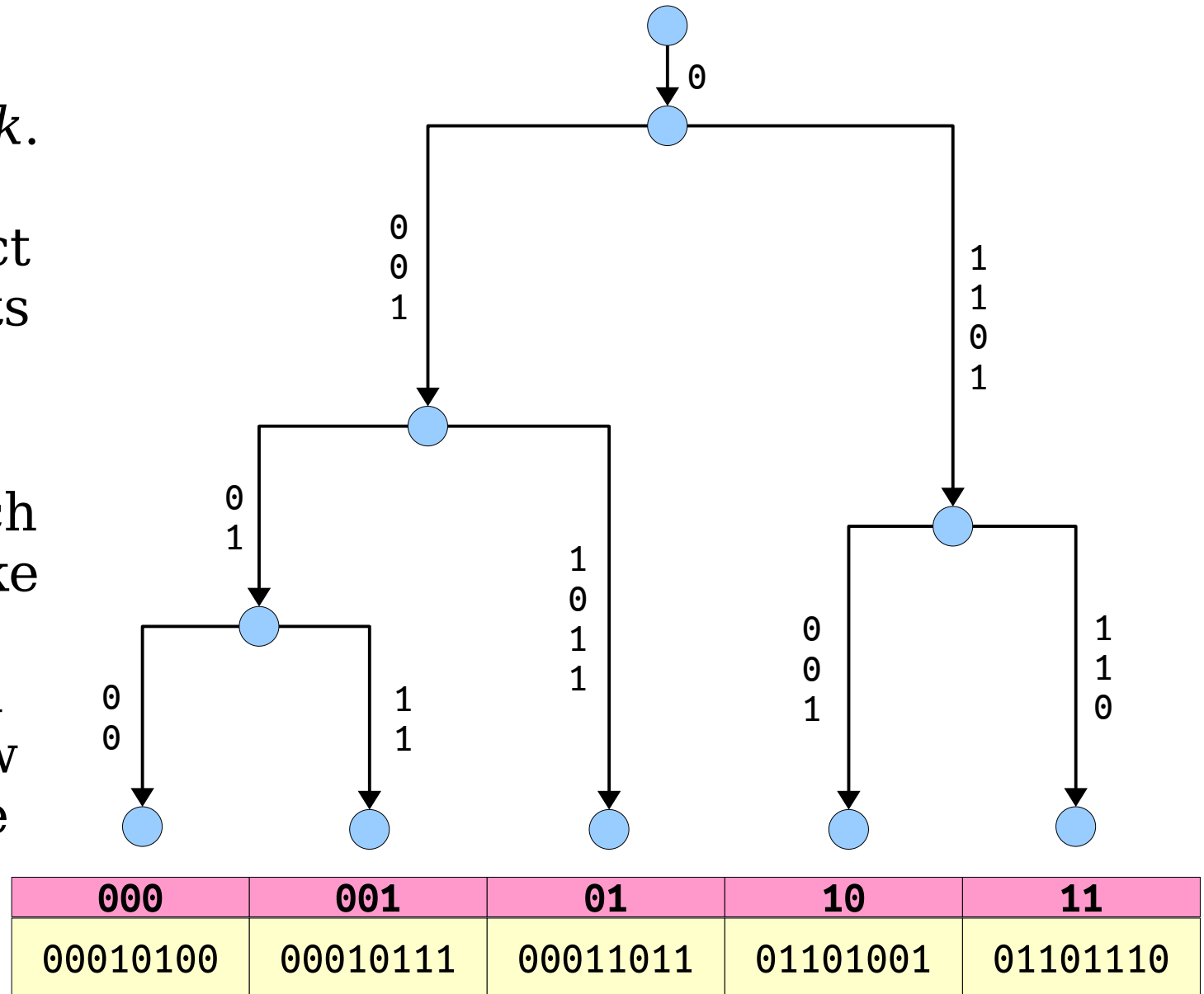
# Back to Tries

- We're ultimately interested in compressing our numbers so they all fit in a machine word.
- There are at most  $w^\epsilon$  bits in each of these new numbers – that's really promising!



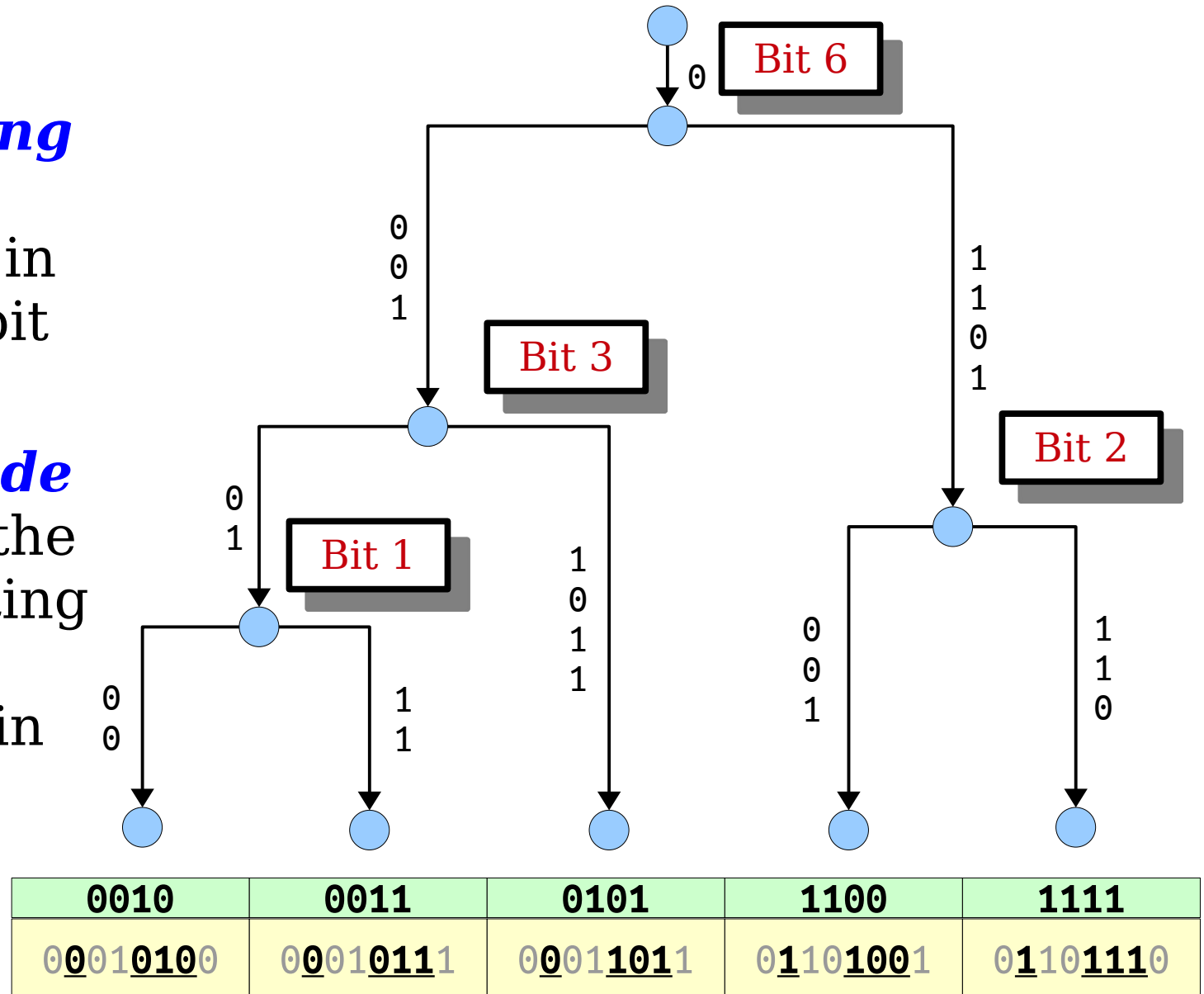
# Back to Tries

- Suppose we get some query key  $k$ . Which bits of  $k$  should we extract from it to form its code?
- **Problem:** This depends on which path it would take through the Patricia trie, and it's not clear how to do this in time  $O(1)$ .



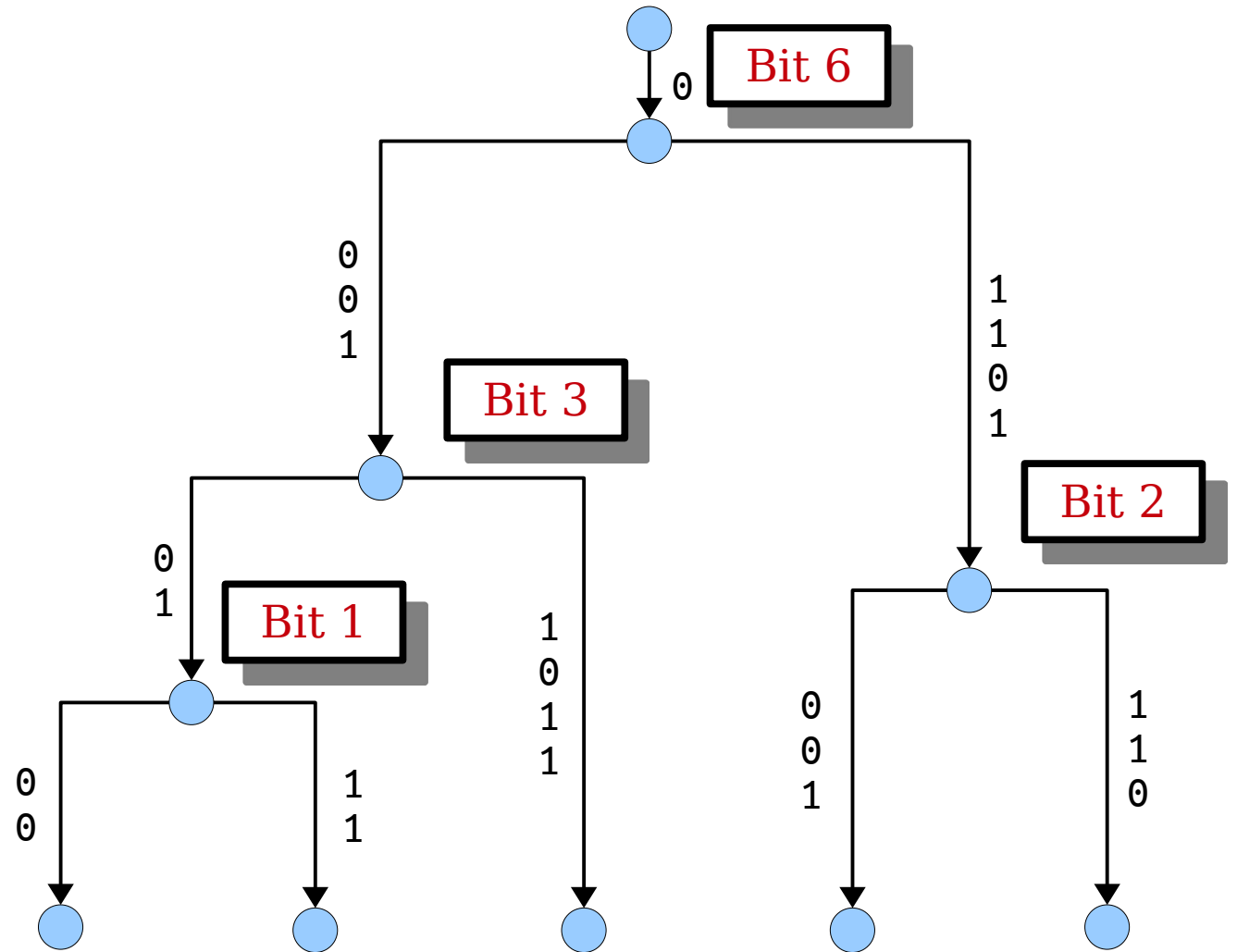
# Patricia Codes

- A bit index  $i$  is called *interesting* if there is a branching node in the trie at that bit index.
- The *Patricia code* of an integer is the bitstring consisting of just the interesting bits in that number.



# Patricia Codes

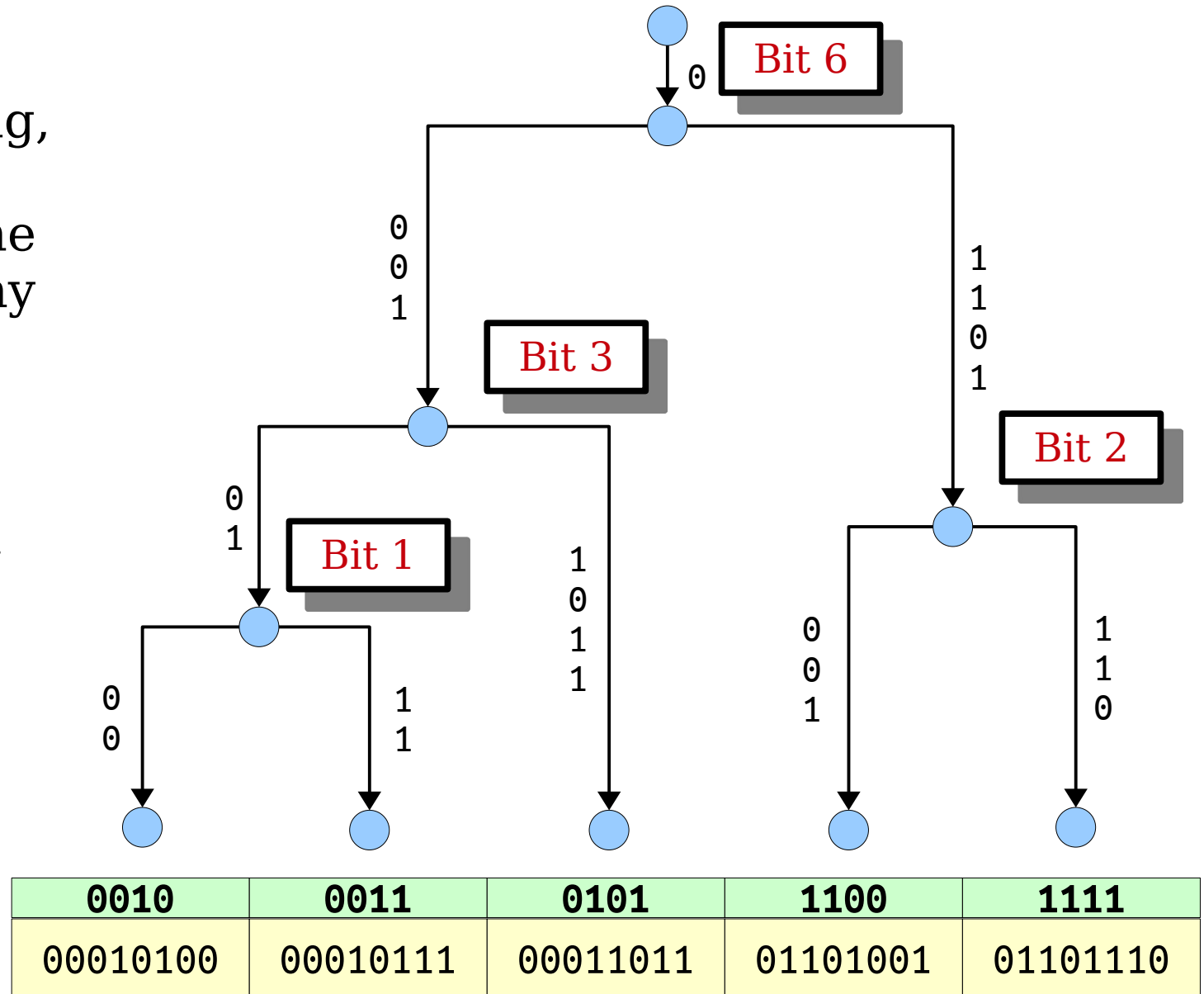
- **Claim:** The relative order of the integers in this trie is the same as the relative numeric order of their Patricia codes.
- Each bit either gives a direction to branch at a decision point, or is in the middle of an edge and doesn't matter.



0010	0011	0101	1100	1111
00010100	00010111	00011011	01101001	01101110

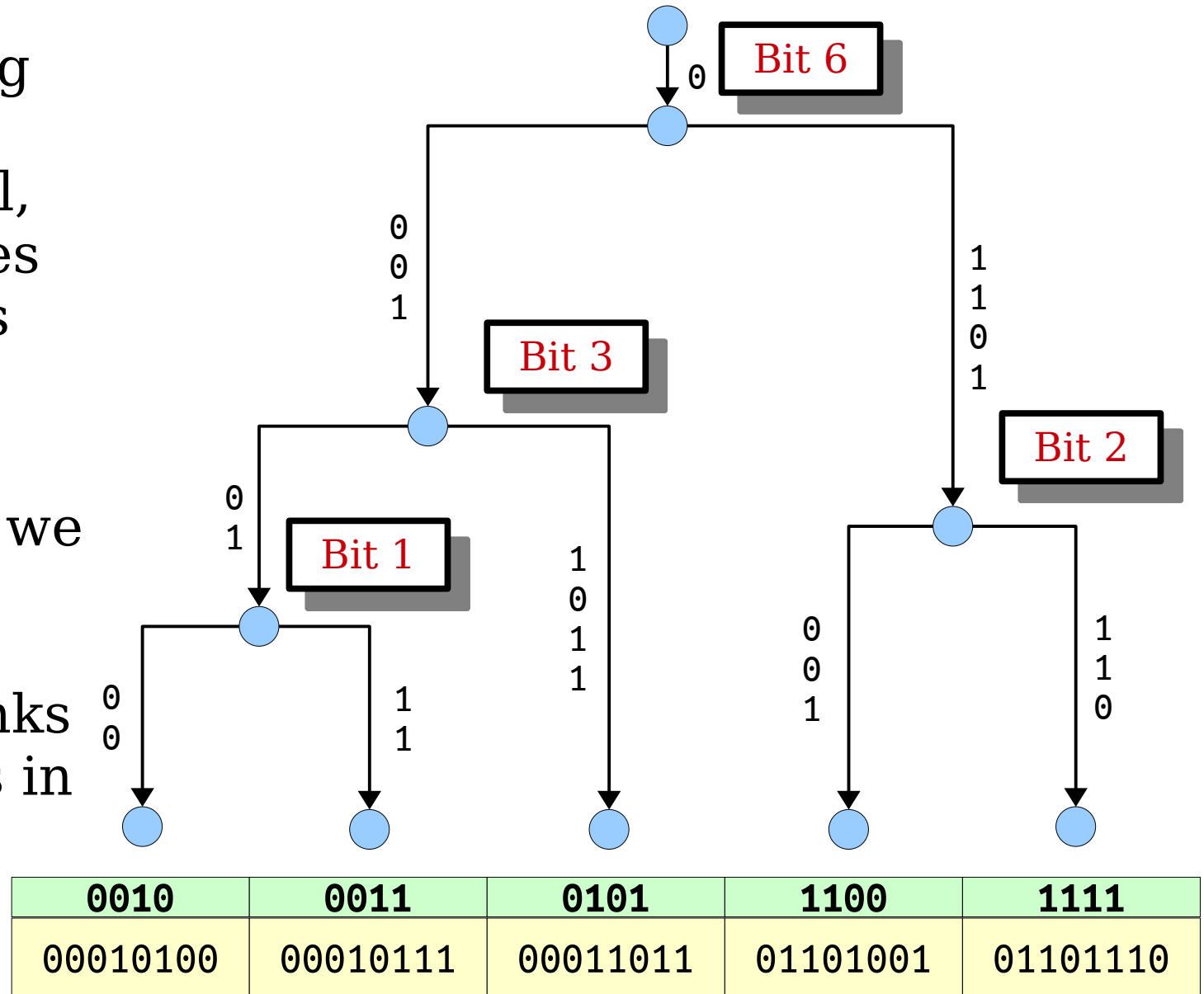
# Patricia Codes

- **Claim:** With the right preprocessing, there's a way to (sorta) compute the Patricia code of any number in time  $O(1)$ .
- We'll go over the details later today.



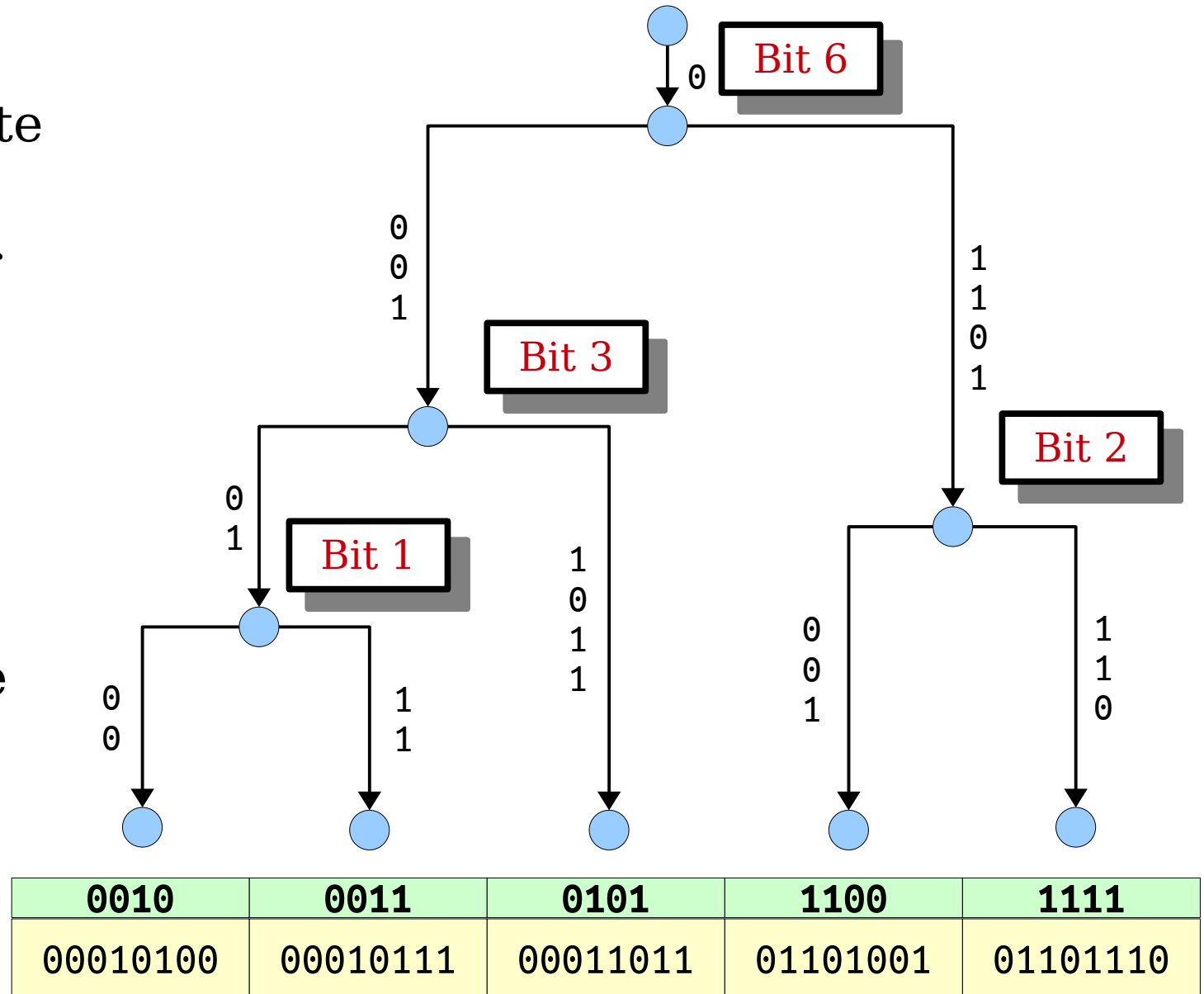
# Patricia Codes

- **Claim:** Assuming we pick  $\varepsilon$  to be sufficiently small, the Patricia codes for our  $w^\varepsilon$  values will fit into a machine word.
- This means that we can preprocess them so that we can compute ranks of Patricia codes in time  $O(1)$ .



# Patricia Codes

- Our goal is to efficiently compute ranks among the original numbers.
- If all our Patricia codes fit into a single machine word, we can compute *rank*(x) in time  $O(1)$ , though it's a little trickier than it looks.

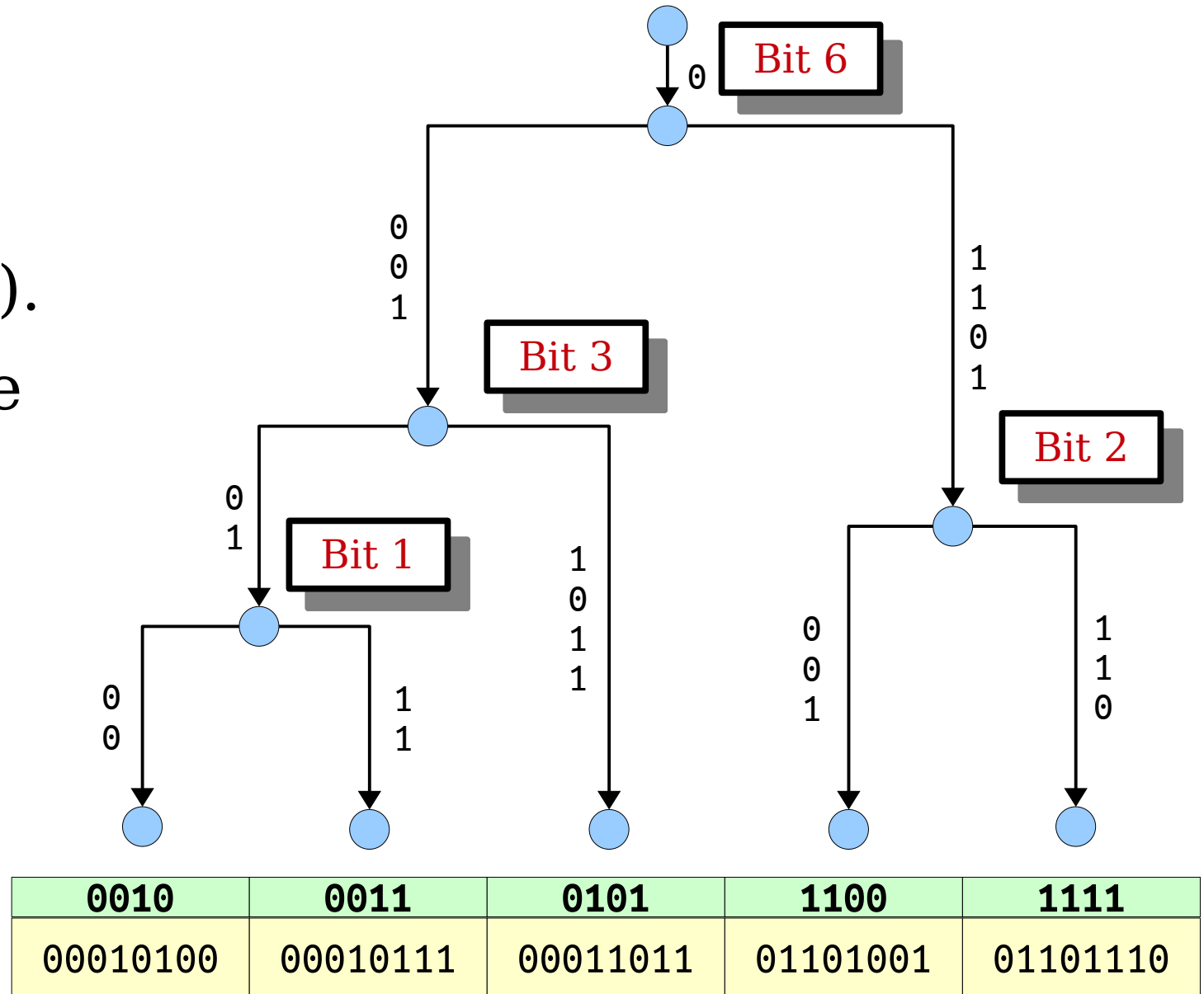




# Computing Ranks

- Suppose we want to determine *rank*(00010101).
- First, compute its Patricia code:

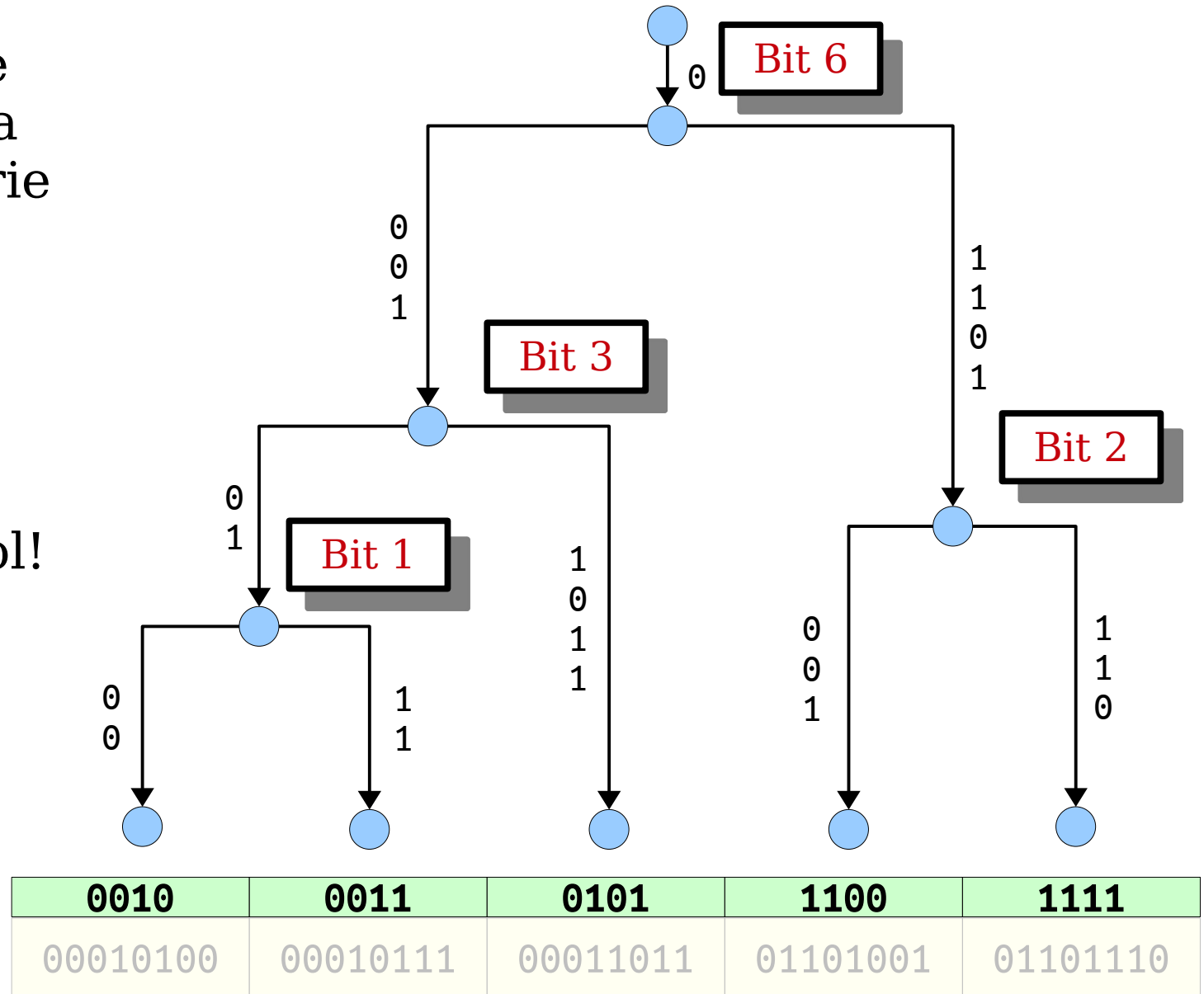
<b>0010</b>
0 <u>0</u> 01 <u>0</u> 101



# Computing Ranks

- Now, compute the rank of its Patricia code across the trie elements.
- Notice that the rank of this number matches the rank of its Patricia code. Cool!

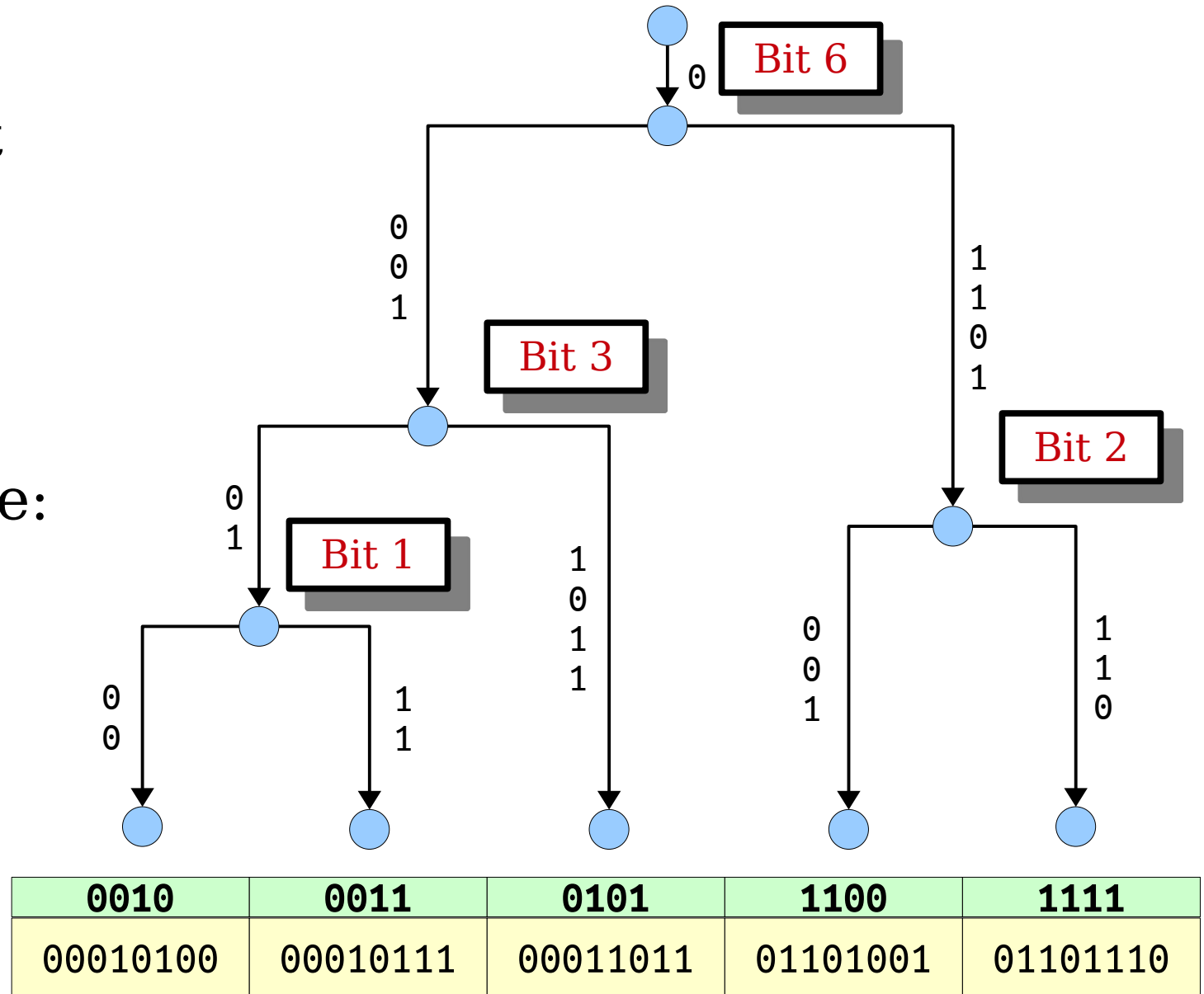
<b>0010</b>
00010101



# Computing Ranks

- Unfortunately, things get a bit trickier here. Let's compute *rank*(01001110).
- First, compute its Patricia code:

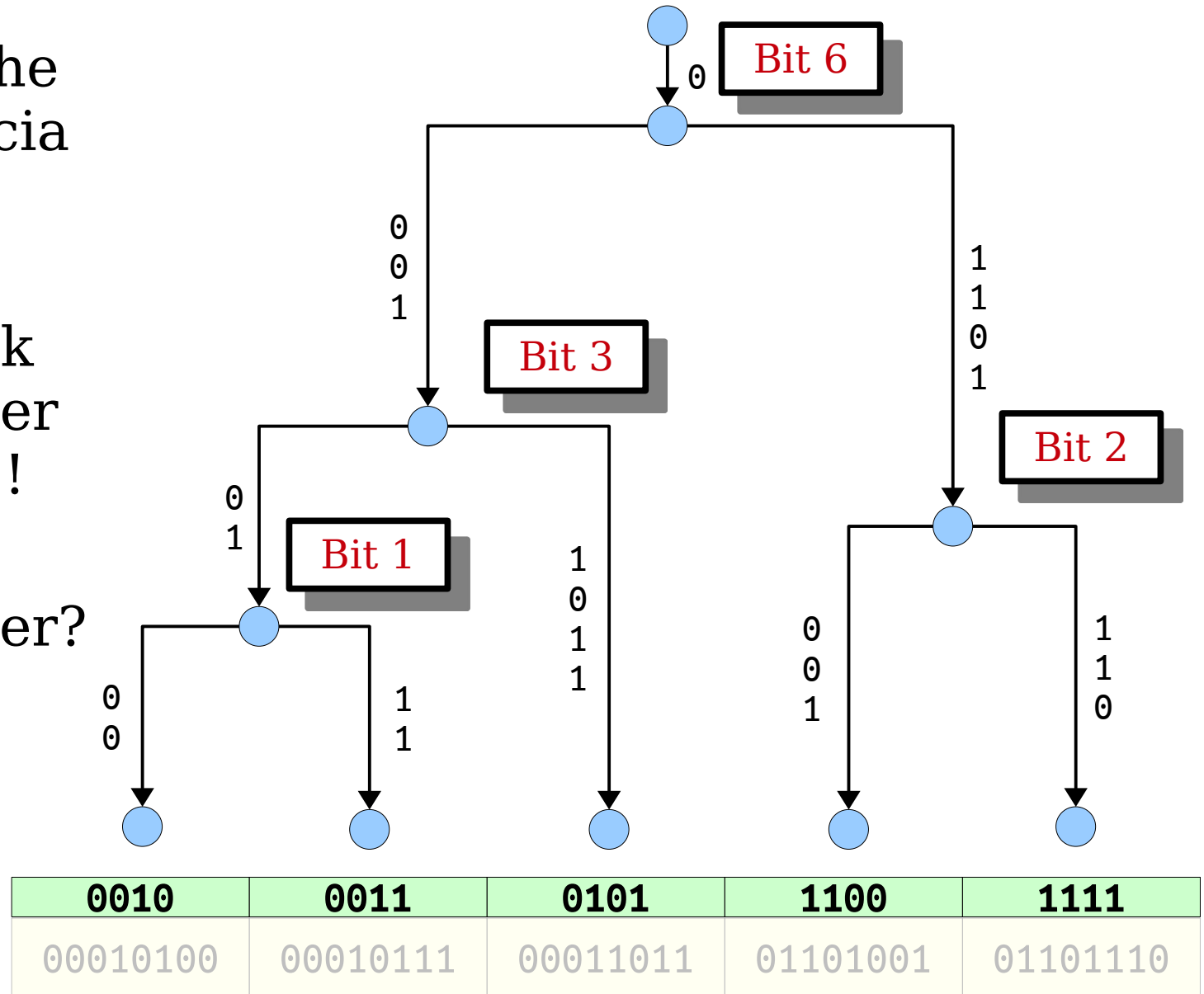
<b>1111</b>
0 <u>1</u> 00 <u>111</u> 0



# Computing Ranks

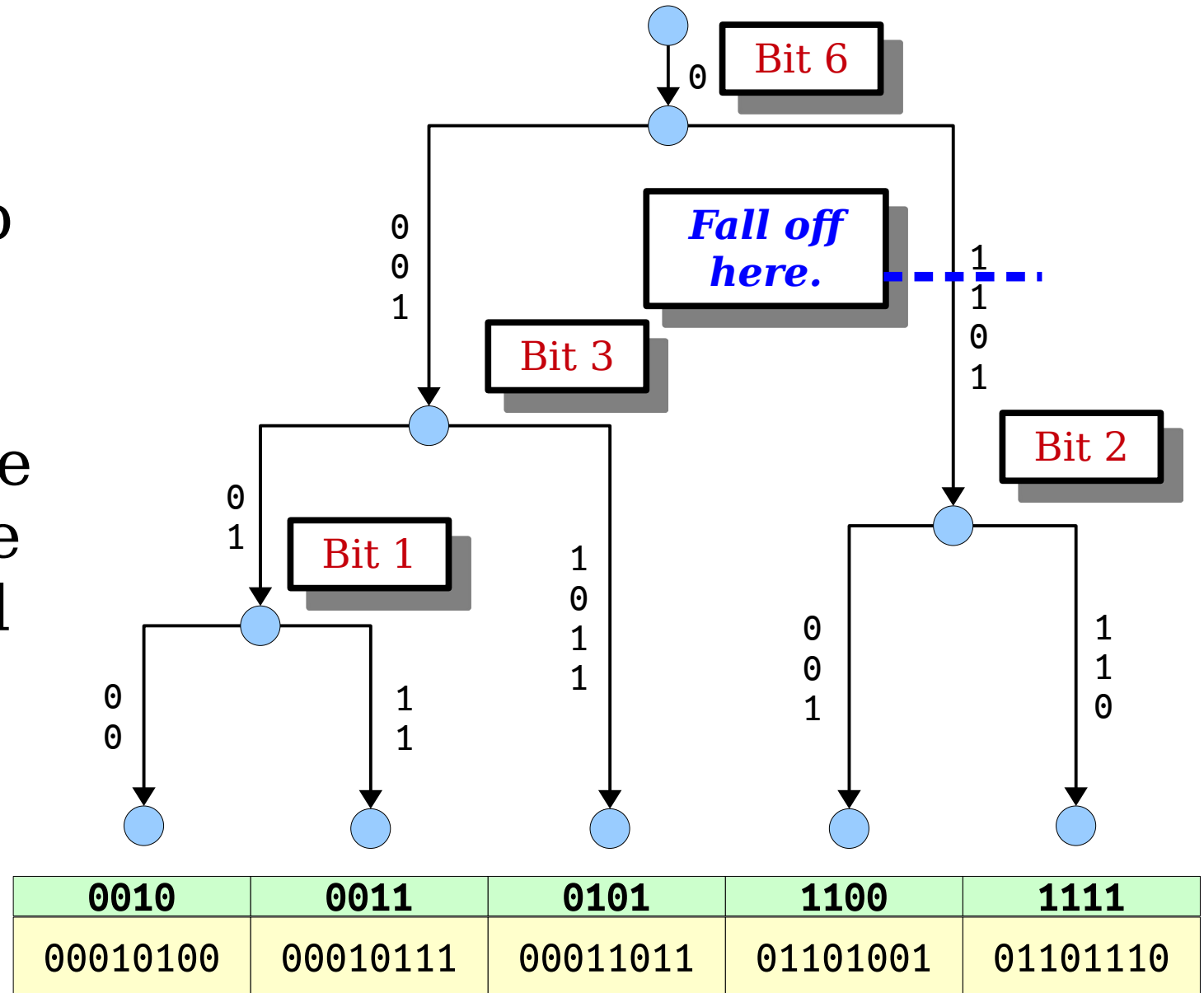
- Now, compute the rank of its Patricia code across the trie elements.
- Its code has rank 5, but the number itself has rank 3!
- Why did we get the wrong answer?

<b>1111</b>
01001110



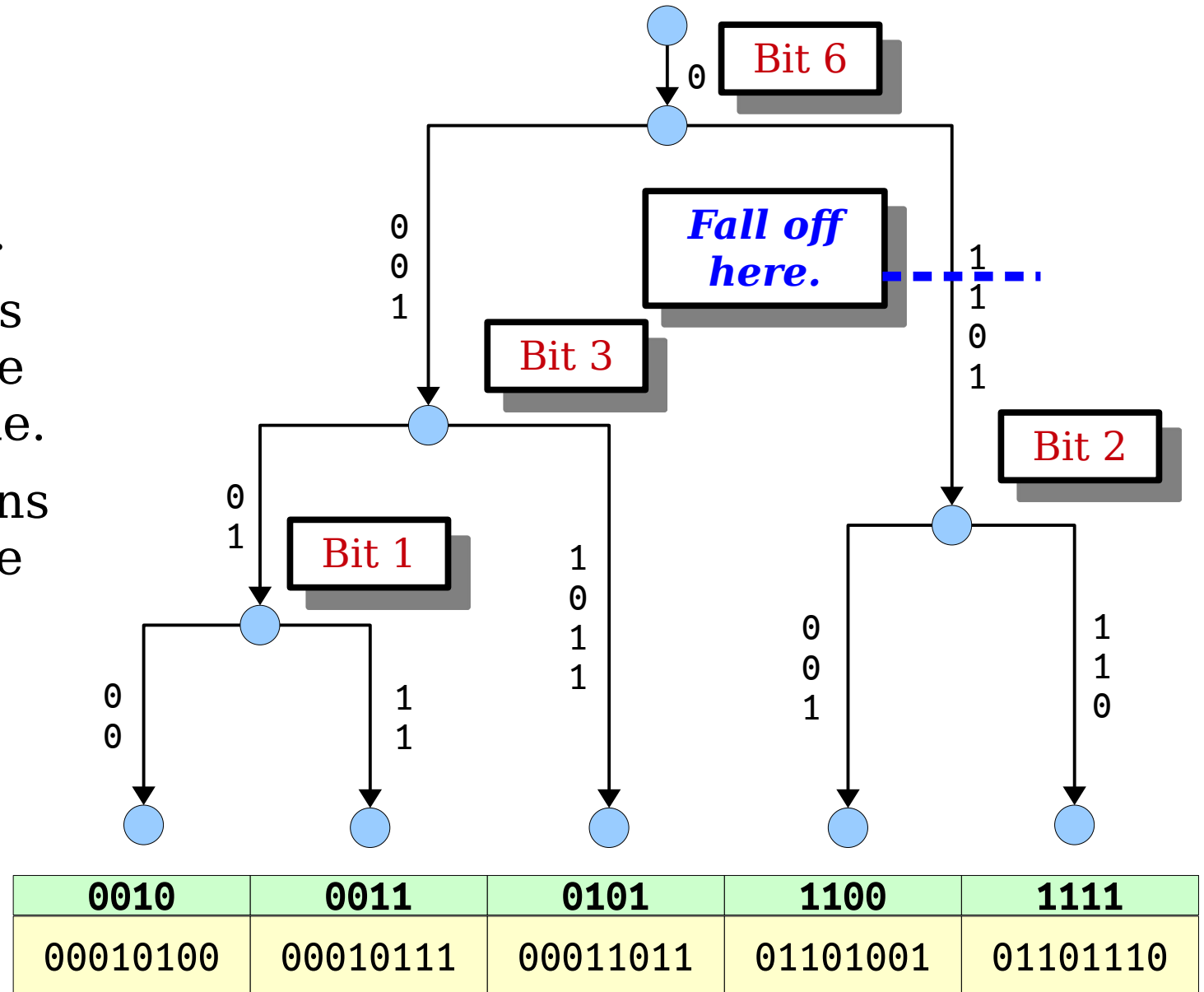
# Computing Ranks

- Imagine we did a real, proper lookup of this key in the trie.
- Notice that we fall off the trie at the marked point.



# Computing Ranks

- We made some “good” decisions followed by some “bogus” decisions.
- The good decisions are the ones where we were on the trie.
- The bogus decisions were from after we fell off.

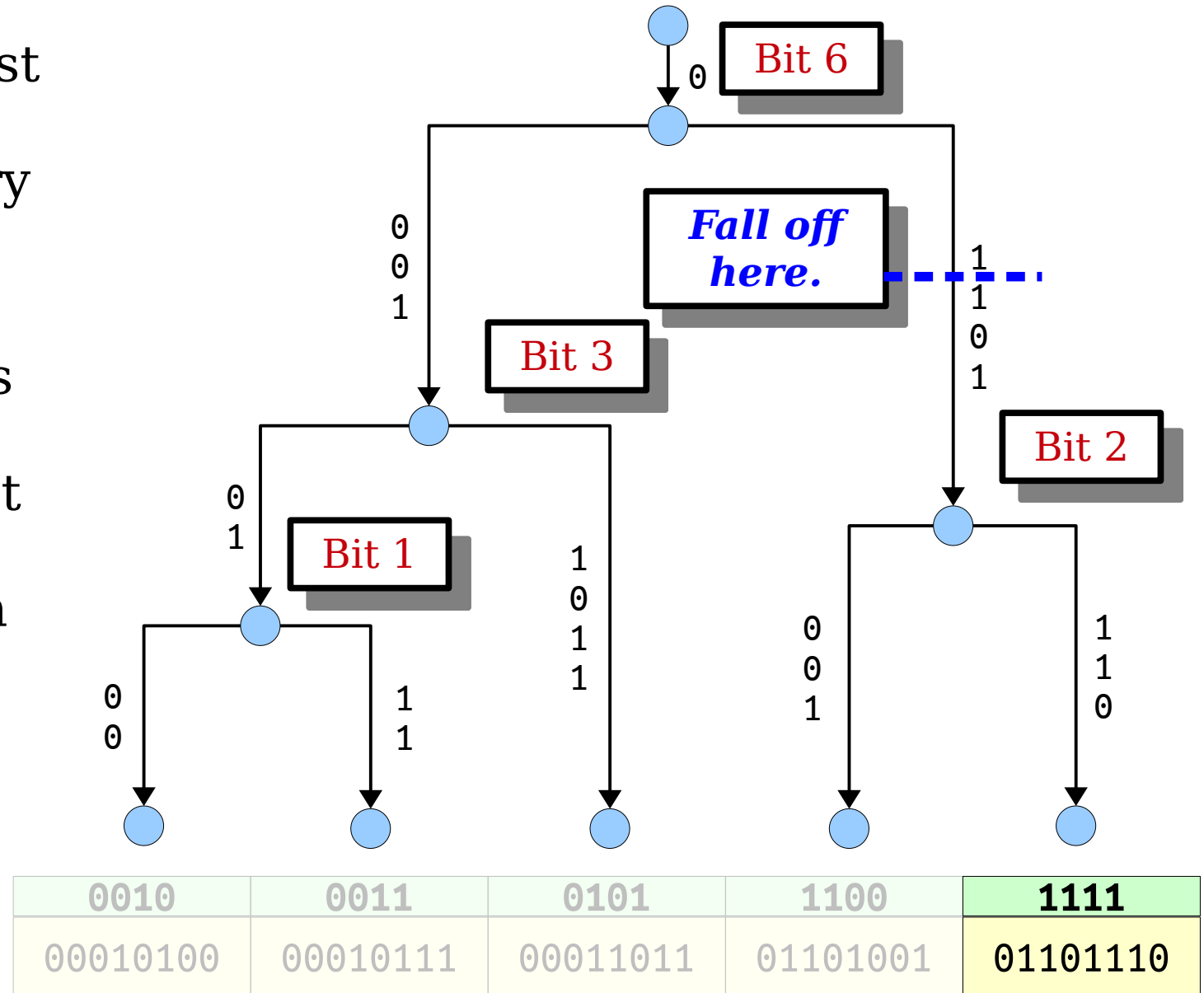


<b>1111</b>
01001110

# Computing Ranks

- Look at the longest common prefix between our query key and the key next to it.
- Since the LCP has length two, we know that the first two bits of our number stayed on the trie, and then we fell off.

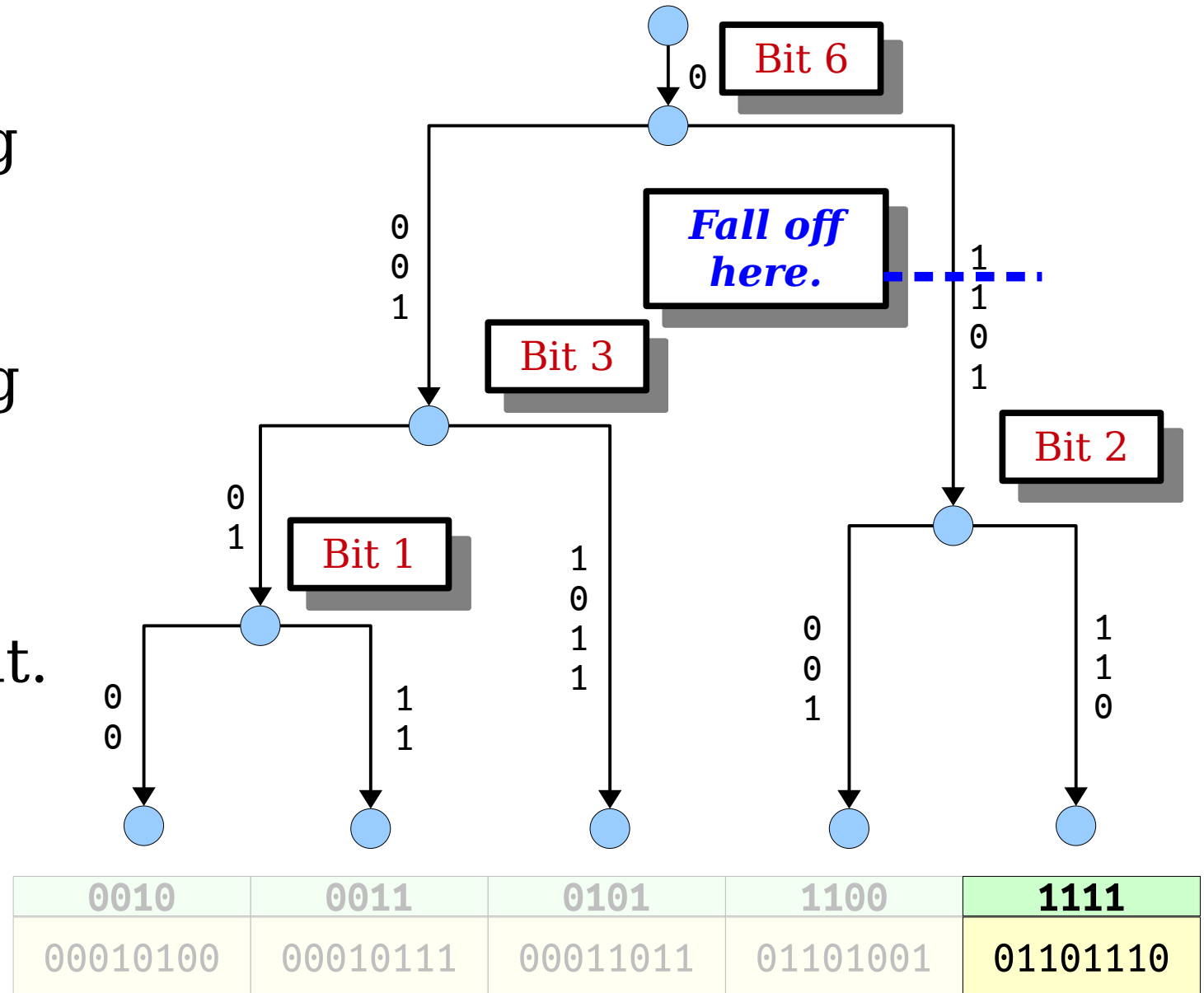
<b>1111</b>
01001110



# Computing Ranks

- We fell off the trie by reading a 0.
- That means that we belong *before* everything in the subtree after that point.

<b>1111</b>
01001110

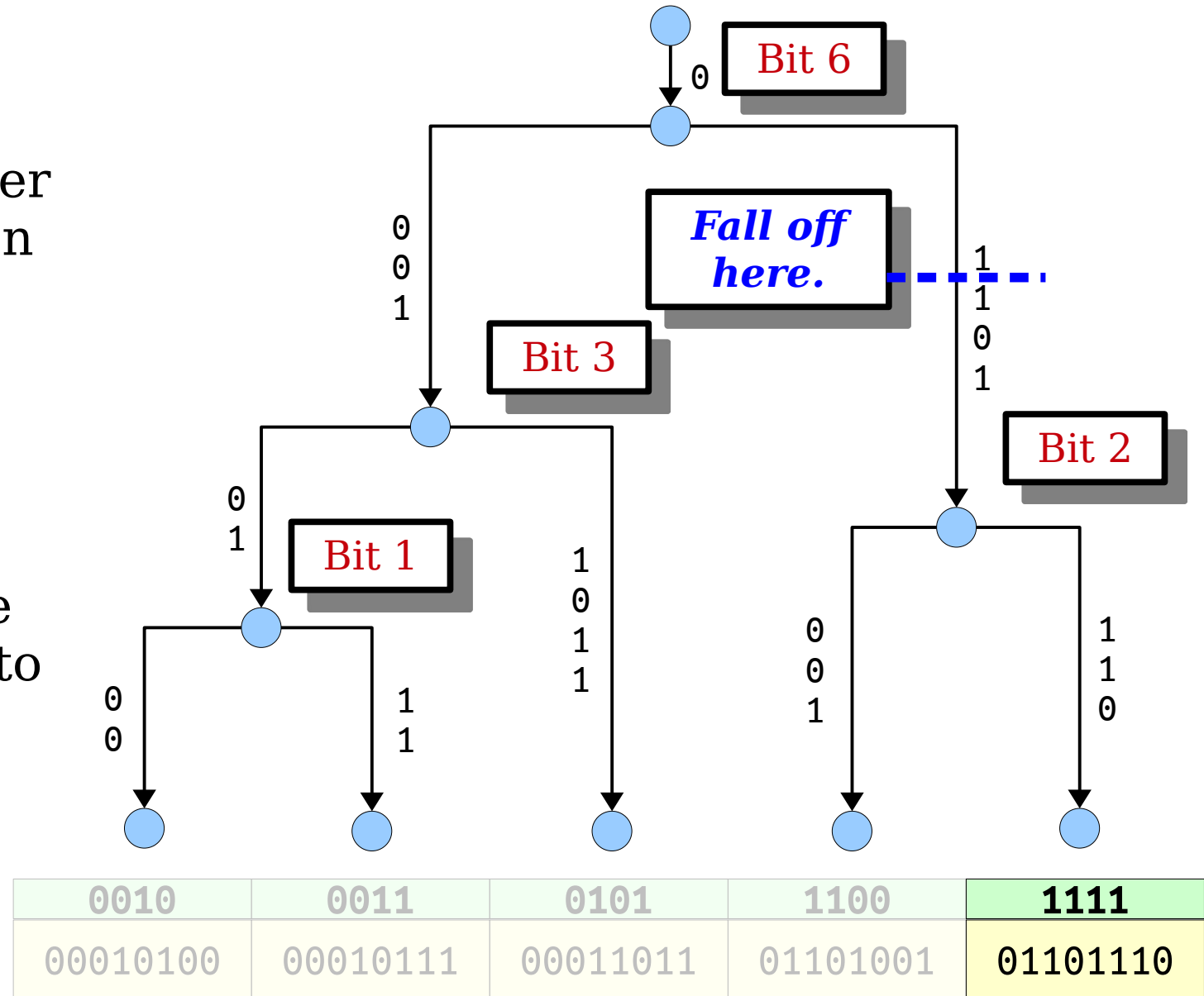




# Computing Ranks

- **Idea:** Change our number to put a 0 in all positions after the mismatch, then recompute the Patricia code.
- This means “all previous comparisons are good, and then we lose on tiebreaks to everything else.”

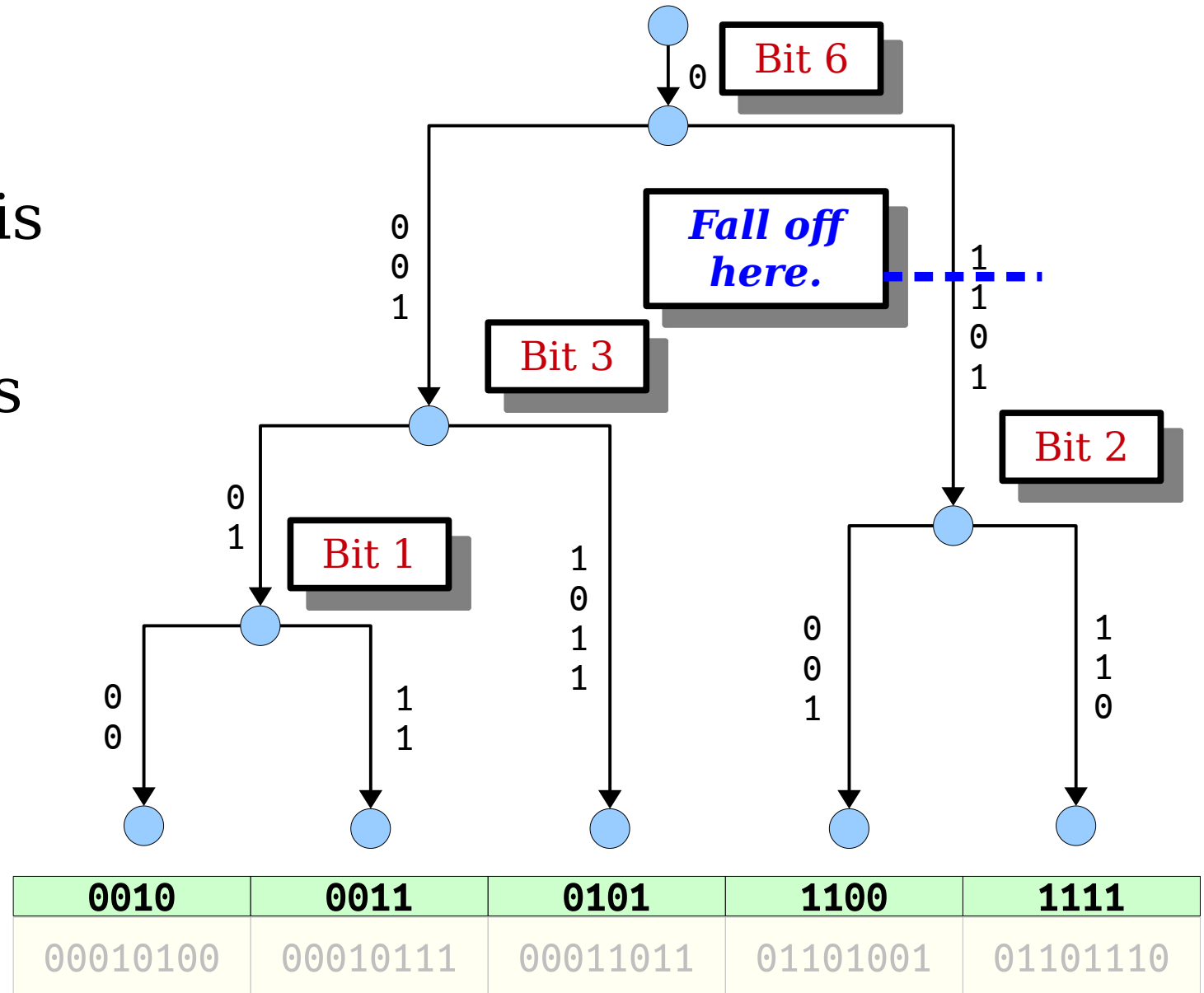
**1000**  
01000000



# Computing Ranks

- Let's do a *second* rank query with this new code.
- That places us at rank 3, which is the proper position.

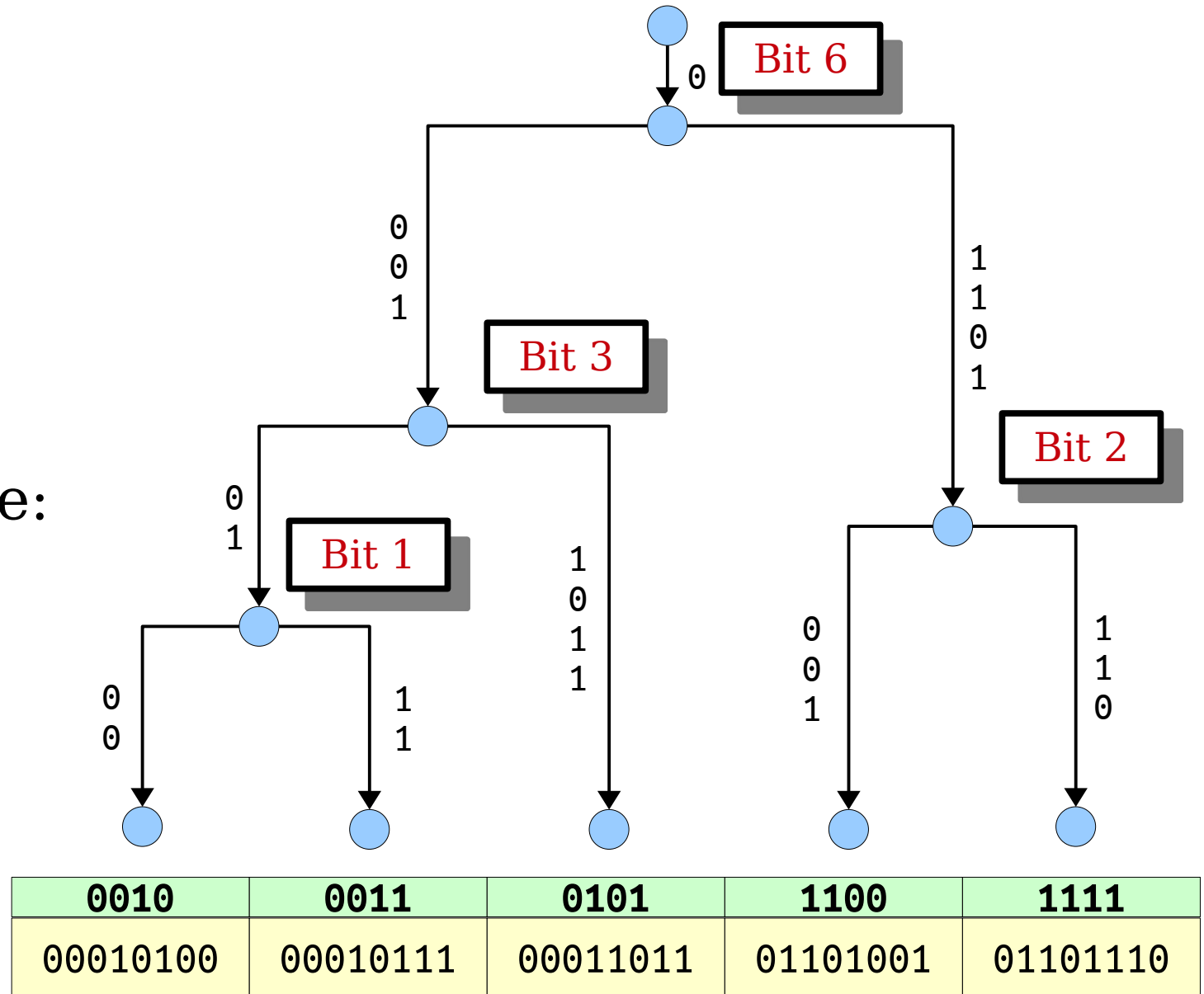
<b>1000</b>
01001110



# Computing Ranks

- Let's show this idea in action again by computing *rank*(10000000).
- First, compute its Patricia code:

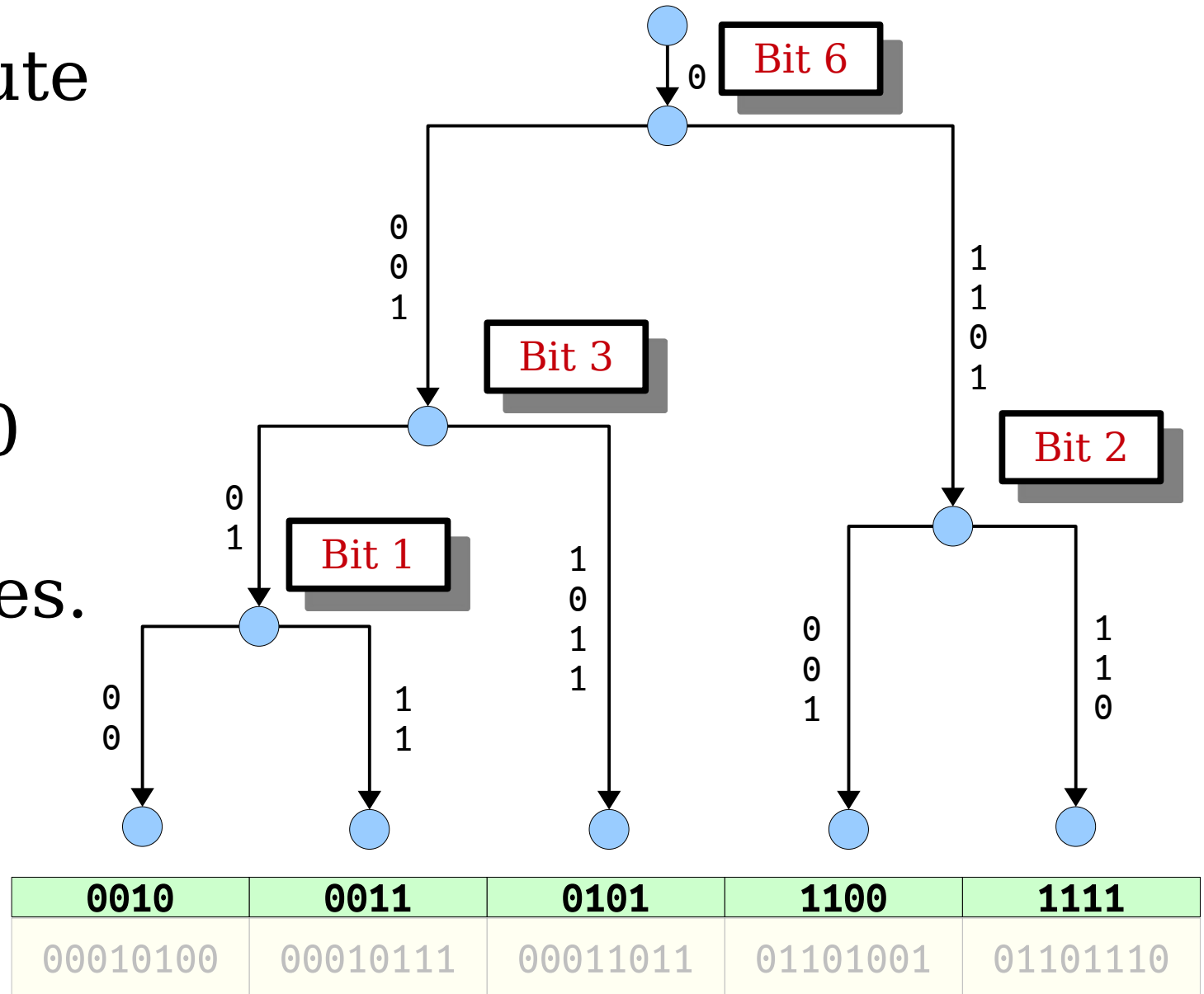
0000
1 <u>0</u> 00 <u>0000</u>



# Computing Ranks

- Next, compute the rank of the Patricia code.
- It has rank 0 among Patricia codes.

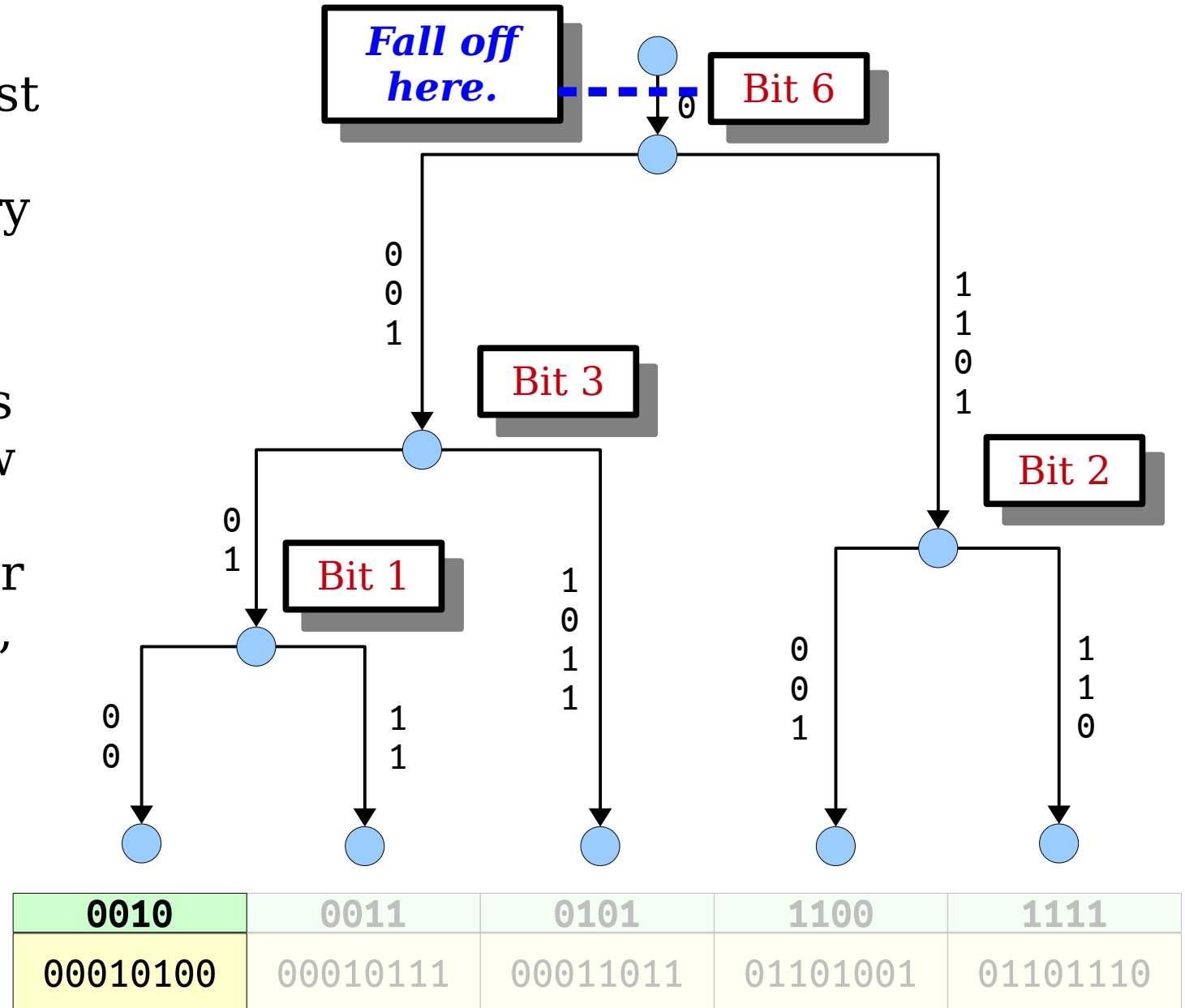
<b>0000</b>
<b>10000000</b>



# Computing Ranks

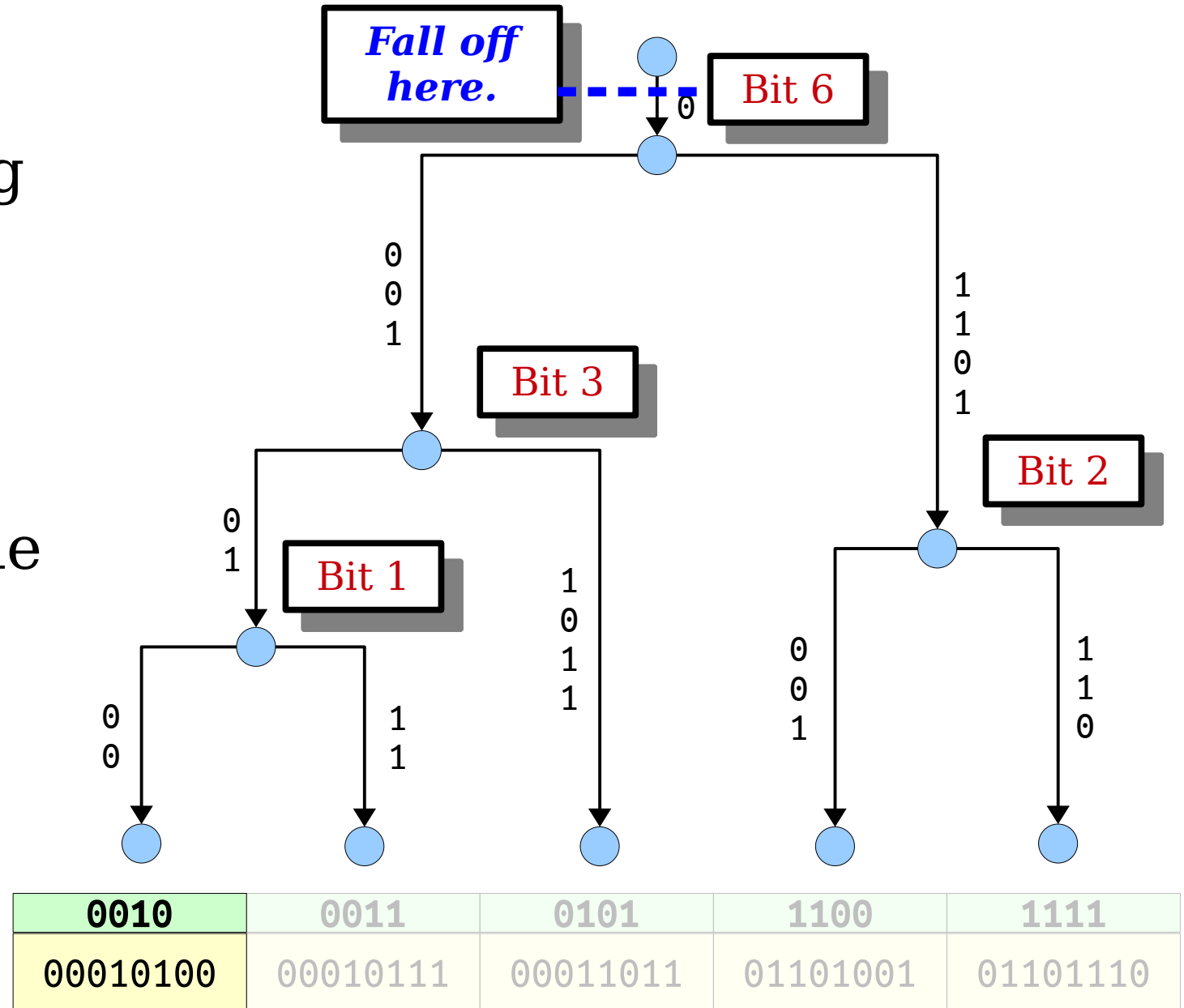
- Look at the longest common prefix between our query key and the key next to it.
- Since the LCP has length 0, we know that the first zero bits of our number stayed on the trie, and then we fell off.

0000
10000000



# Computing Ranks

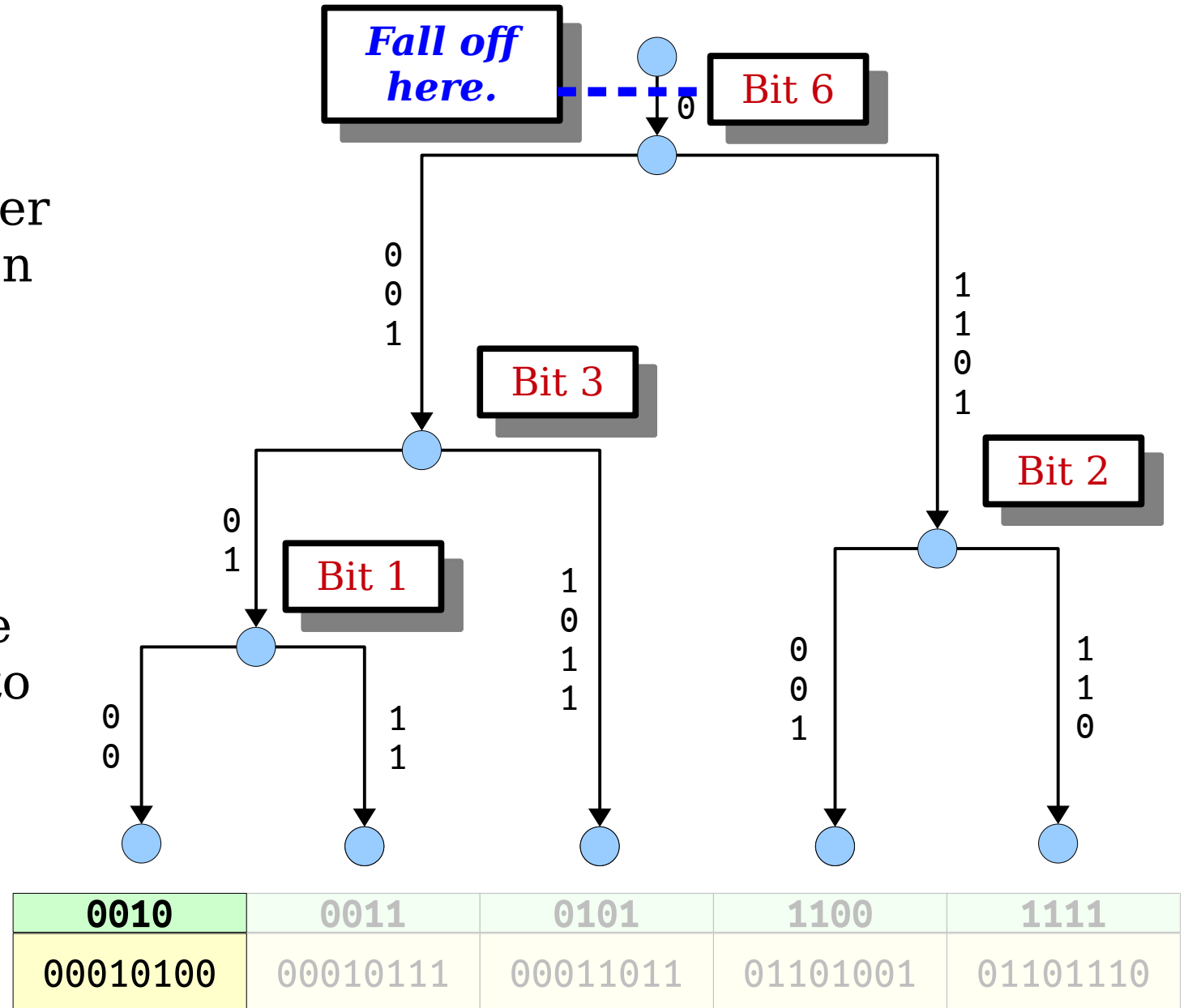
- We fell off the trie by reading a 0.
- That means that we below *after* all the elements in the subtree after that point.



# Computing Ranks

- **Idea:** Change our number to put a 1 in all positions after the mismatch, then recompute the Patricia code.
- This means “all previous comparisons are good, and then we win on tiebreaks to everything else.”

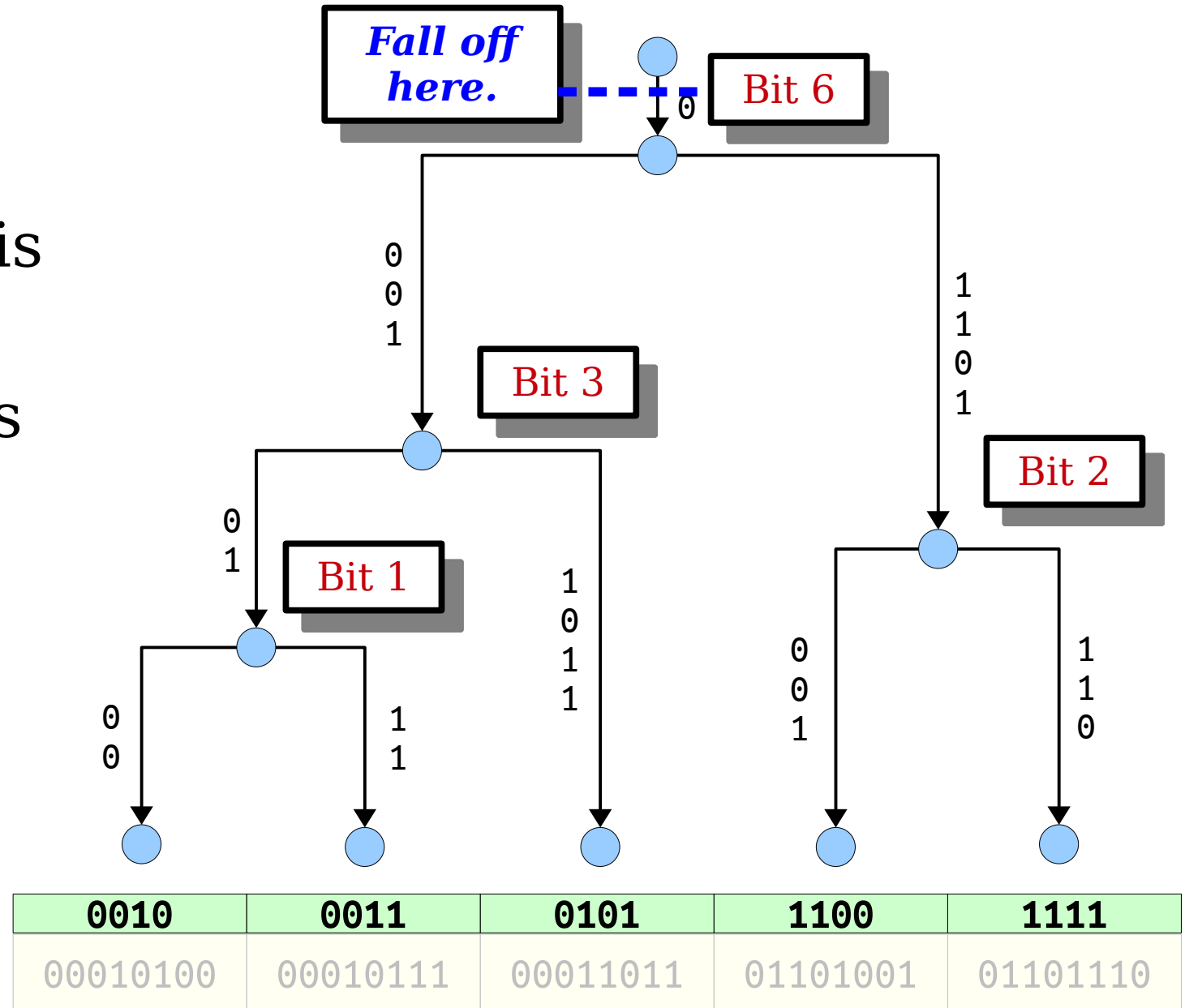
**1111**  
11111111



# Computing Ranks

- Let's do a *second* rank query with this new code.
- That places us at rank 5, which is the proper position.

<b>1111</b>
10000000





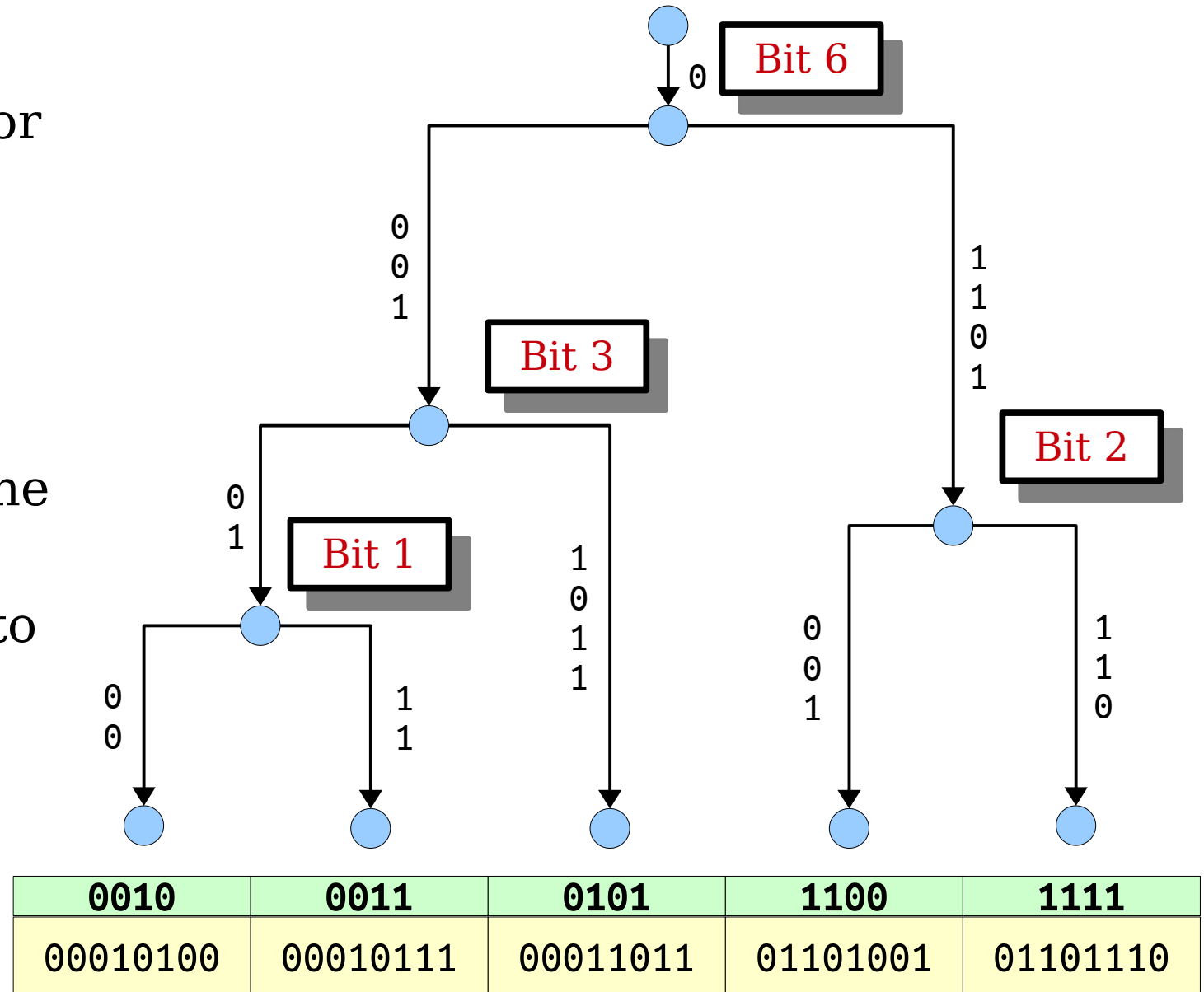
# Rank in $O(1)$

- To search for a key:
  - Compute its Patricia code.
  - Use a *parallel rank* to determine the rank of its Patricia code.
  - Use our `msb` function from earlier to determine the longest matching prefix between the key and the values adjacent to it.
  - Based on the next bit, either replace all successive bits in the Patricia code either with 0s or with 1s.
  - Run a second *parallel rank* to determine the actual rank of the element in the sequence.
- Total cost:  **$O(1)$** .
- I'm glossing over a few details here; check the original paper for details.

# Implementing our Patricia Trie Scheme

# Implementing this Idea

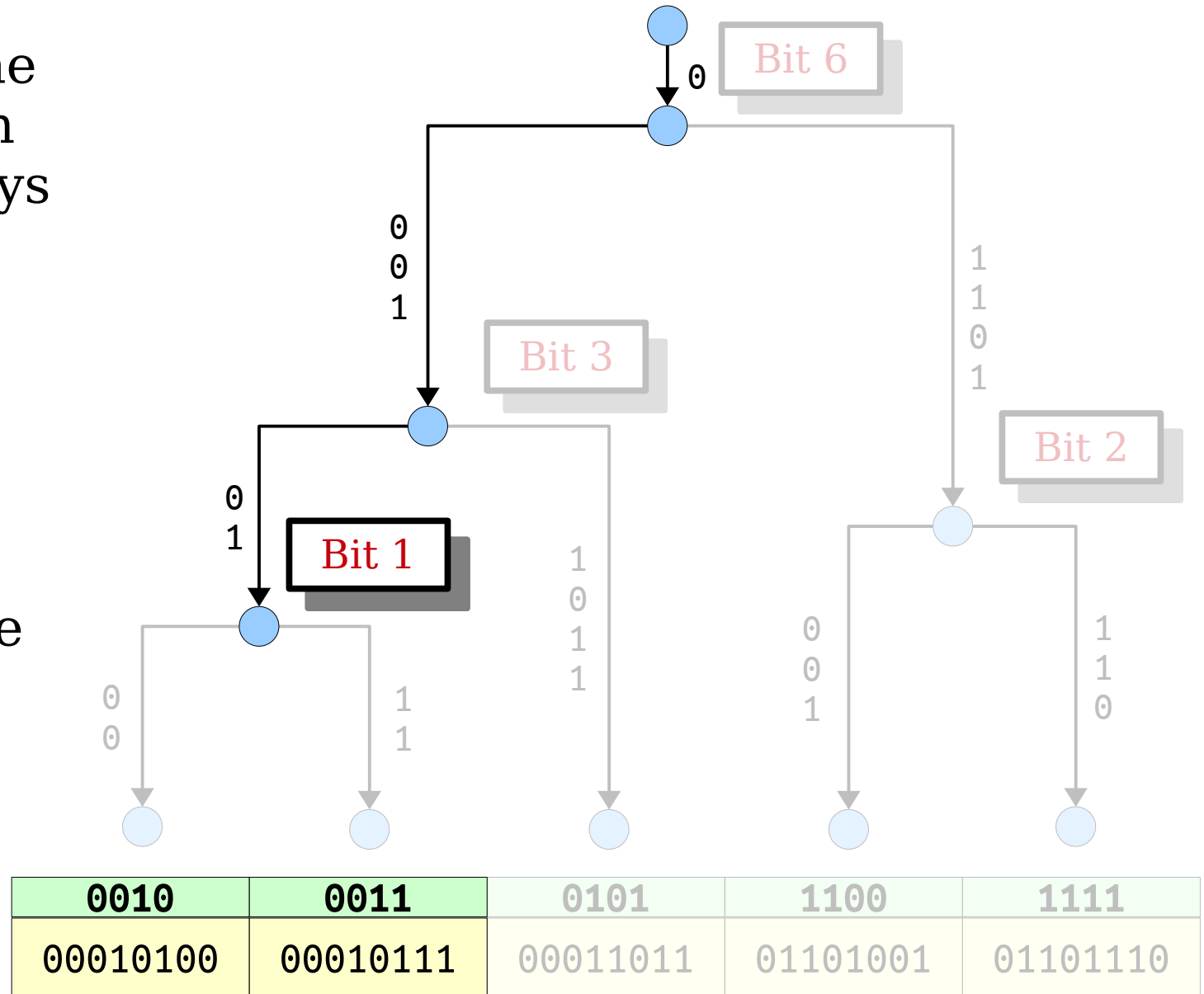
- We now have a clever approach for compressing keys based on Patricia tries.
- In this discussion, I've drawn the actual trie off to the side here.
- We used this trie to determine where the "interesting" bits were.



# Implementing this Idea

- We can find all the interesting bits in a collection of keys without actually building this trie.
- **Idea:** There's a connection between branching nodes in the trie and the lcp's of the keys.

00010100
00010111



# Implementing this Idea

- Since we don't need the Patricia trie, we can cast it off into the luminiferous aether.
- We can just store the indices of the interesting bits and the Patricia codes of the keys.



0010	0011	0101	1100	1111
00010100	00010111	00011011	01101001	01101110

# Implementing this Idea

- We've assumed up to this point that we can compute Patricia codes in time  $O(1)$ .
- This is the last step we need to figure out!
- How do we do this?

Bit 6

Bit 3

Bit 2

Bit 1

0010	0011	0101	1100	1111
00010100	00010111	00011011	01101001	01101110

# Extracting Patricia Codes

- We'd like to extract the  $w^\epsilon$  interesting bits from each machine word, and ideally, to do so quickly.
- We can start by building up a bitmask to mask everything except those interesting bits.
- If we can compact these bits together, we've got the Patricia code!

00011010 01101110 01111000 01001101 00101111 00001101 01110111 01100001

00010000 00000000 00010100 00000000 00010000 00000010 00000000 00000001

00010000 00000000 00010000 00000000 00000000 00000000 00000000 00000001





# Extracting Patricia Codes

- The approach we used last time worked well because we knew those bits were evenly-spaced.
- **Problem:** Our “interesting” bits aren’t well-spaced across the word in question.
- This may make it impossible to get all the bits next to one another purely using a clever multiplication.

a000b000c000d000e000f000g000h000ijkl

- Fortunately, there’s an escape hatch.

# Approximate Patricia Codes

- Patricia codes are useful because they
  - contain enough information to compute ranks, and
  - compact that information into a small space.
- **Idea:** Maintain the second property by doing a “decent” job compacting bits, rather than a “perfect” job.

000a0000 00000000 000b0c00 00000000 000d0000 000000e0 00000000 0000000f

a00000b00c0d00e000000f

# Approximate Patricia Codes

- An **approximate Patricia code** is a bitstring containing all the interesting bits of a number in the same relative order, with some extra 0's deterministically interspersed.
- **Claim:** We can use approximate Patricia codes rather than true Patricia codes to compute ranks. The relative orders of the codes will come back the same.

000a0000 00000000 000b0c00 00000000 000d0000 000000e0 00000000 0000000f

a00000b00c0d00e000000f

# Approximate Patricia Codes

- **Theorem:** Suppose we have a  $w^\epsilon$  interesting bits. Then there is a way to compute a multiplier  $M$ , a mask  $K$ , and a shift  $S$  such that

$$((n \times M) \gg S) \& K$$

is an approximate Patricia code for  $n$  that uses  $w^{4\epsilon}$  bits, and these values can be computed in time  $O(w^{4\epsilon})$ .

- **Proof idea:** Create a window of size  $(w^\epsilon)^3$ . This gives enough slack space to provide a place to shift each individual bit with no overlaps. Why? Because for each new bit, the possible conflicts you have to worry about depend purely on the other bits placed so far (fewer than  $w^\epsilon$ ), the other bits in the number (at most  $w^\epsilon$ ), and the offsets assigned to the other placed bits (at most  $w^\epsilon$ ). Therefore, there are fewer than  $(w^\epsilon)^3$  constraints, so having  $(w^\epsilon)^3$  slots suffices. From there, we multiply in one last factor of  $w^\epsilon$  replicating the window to ensure that the bits get spread out in the right order.
- Thanks to former CS166 student Jane Lange for this explanation!

# Closing In on Fusion Trees

- Our goal is to build a data structure that holds  $w^\varepsilon$  integers with  $w$  bits each in a way that supports *rank* in time  $O(1)$ .
- Given  $w^\varepsilon$  integers, we can do some preprocessing to form  $w^{4\varepsilon}$ -bit approximate Patricia codes for them.
- Storing those approximate codes requires  $w^{5\varepsilon}$  bits.
- **Observation:** Suppose we pick  $\varepsilon = 1/6$ . Then we can store all of those codes in a single machine word!

What is  $w^{1/6}$  on a real computer?  
We have a ways to go before this strategy will have any chance of being practical.

# Fusion Trees

- A ***fusion tree*** is a B-tree augmented with the preceding strategy for computing ranks quickly.
- The B-tree has order  $w^{1/6}$ , so its height is  $O(\log_w n)$ .
- Since the rank of a key in a node can be computed in time  $O(1)$ , the cost of a lookup, predecessor, or successor operation is  $O(\log_w n)$ .

# Fusion Trees

- Here's the final scorecard for fusion trees.
- Notice that **lookup** and **successor** queries are unconditionally asymptotically faster than a regular balanced BST!

## The Fusion Tree

- **lookup**:  $O(\log_w n)$
- **insert**:  $O(w^{2/3} \log_w n)$
- **delete**:  $O(w^{2/3} \log_w n)$
- **max**:  $O(\log_w n)$
- **succ**:  $O(\log_w n)$
- Space:  $\Theta(n)$

# Fusion Trees

- The mutating operations *insert* and *delete* are expensive.
- The costs here arise from the cost of recomputing the multipliers necessary for computing Patricia codes.
- *Can we do better?*

## The Fusion Tree

- *lookup*:  $O(\log_w n)$
- *insert*:  $O(w^{2/3} \log_w n)$
- *delete*:  $O(w^{2/3} \log_w n)$
- *max*:  $O(\log_w n)$
- *succ*:  $O(\log_w n)$
- Space:  $\Theta(n)$



# Fusion Trees

- In 1996, Arne Andersson devised the **exponential tree**, a variation on fusion trees with these indicated runtimes.
- It's basically a fusion tree, except the node branching factors decay geometrically from one layer to the next.
- This still keeps the tree height low, but makes the amortized cost of each operation small.

## The Exponential Tree

- **lookup**:  $O(\log_w n)$
- **insert**:  $O(\log_w n + \log \log n)^*$
- **delete**:  $O(\log_w n + \log \log n)^*$
- **max**:  $O(\log_w n)$
- **succ**:  $O(\log_w n)$
- Space:  $\Theta(n)$

\* Amortized

A Cool Application: ***Integer Sorting***

# Integer Sorting

- You're given a list of integers  $x_1, x_2, \dots, x_n$  to sort, and each fits into a machine word.

- **Heapsort** takes time  $O(n \log n)$ .
- **Base-2 radix sort** takes time  $O(nw)$ .
- **Base- $n$  radix sort** takes time  $O(nw / \log n)$ .

“Classical”  
techniques

- **Y-fast trie sort** takes (expected) time  $O(n \log w)$ .
- **Exponential tree sort** takes time  $O(n \log_w n + n \log \log n)$ .

“Modern”  
techniques

# Integer Sorting

- These algorithms are asymptotically incomparable, since  $w$  and  $n$  are independent quantities.

## ***y-Fast Trie Sort***

$O(n \log w)$

## ***Exponential Tree Sort***

$O(n \log_w n + n \log \log n)$

- **Question:** What is the crossover point?

$$n \log w = n \log_w n$$

$$\log w = \log_w n$$

$$\log w = \frac{\log n}{\log w}$$

$$\log^2 w = \log n$$

$$\log w = \sqrt{\log n}$$

# Integer Sorting

- These algorithms are asymptotically incomparable, since  $w$  and  $n$  are independent quantities.

## ***y-Fast Trie Sort***

$$O(n \log w)$$

## ***Exponential Tree Sort***

$$O(n \log_w n + n \log \log n)$$

- **Question:** What is the crossover point?

- **Theorem:** There is a randomized,  $O(n \sqrt{\log n})$ -time integer sorting algorithm.
- **Proof:** If  $\log w \geq \sqrt{\log n}$ , use exponential tree sort. Otherwise, use y-fast trie sort.

# More to Explore

- In 1994, Fredman and Willard (the creators of the fusion tree) invented the **AF-heap**, a variation on a Fibonacci heap with **extract-min** taking time  $O(\log n / \log \log n)$  and used it to get a linear time algorithm for computing minimum spanning trees.
- In 1995, Andersson et al adapted the size-reduction techniques from fusion trees to develop **signature sort**, a randomized sorting algorithm for integers. Assuming  $w = \lg^{2+\varepsilon} n$ , it runs in expected time  $O(n)$ .
- In 1997, using the linear-time MST algorithm, Thorup developed a **linear-time algorithm** for undirected SSSP.
- In 2002, Han developed a deterministic  **$O(n \log \log n)$** -time algorithm for integer sorting that uses only linear space, and with Thorup developed a randomized  **$O(n \sqrt{\log \log n})$** -time algorithm for integer sorting that only uses linear space.
- In 2007, Andersson and Thorup developed a deterministic, worst-case efficient integer ordered dictionary with each operation costing  **$O(\sqrt{\frac{\log n}{\log \log n}})$** , which is provably optimal under reasonable assumptions.

# Why This Matters

- These data structures, while primarily of theoretical interest, give a glimpse of what's still out there.
- They also give a feel for how connected everything is – we're using all the topics we've covered across the quarter!
- And they expose some underlying assumptions about our models of computation that we previously might not have paid much attention to!

That's a wrap!



A Huge Word of Thanks

Thanks to all of you!